

JAZMINE BOLOOR

Las Vegas, Nevada | Email: boloor@unlv.nevada.edu

EDUCATION | **MASTER OF SCIENCE IN ELECTRICAL ENGINEERING – UNIVERSITY OF NEVADA, LAS VEGAS (MAY 2021 – PRESENT)**

- Expected Graduation: December 2022

BACHELOR OF SCIENCE IN ELECTRICAL ENGINEERING – UNIVERSITY OF NEVADA, LAS VEGAS (AUGUST 2017 – MAY 2021)

SKILLS & ABILITIES

SKILLS: PCB layout, soldering, wire bonding, FPGA programming

LANGUAGES: C++, Verilog, VHDL, MATLAB

SOFTWARE: Cadence Virtuoso, LTspice, DipTrace, Eagle, PowerWorld, CAD, Quartus

EXPERIENCE | **UNLV RESEARCH ASSISTANT, DR. R JACOB BAKER**
MARCH 2019 - PRESENT

- Designed and tested Printed Circuit Boards (PCBs) on Eagle and Diptrace; soldered surface-mount and through-hole components; wire bonded using K&S Ltd. Dicing Systems Wire Bonder (4526)
- Designed 3D printed models using CAD software
- Transitioned from undergraduate to graduate research assistant

ENGINEERING INTERN, SOUTHWEST GAS
MAY 2019 - AUGUST 2019

- Member of the Distribution Integrity Management Program team, where distribution pipeline leak analysis data is analyzed to optimize leak prevention
- Worked with a team of testers on the launch of a new application that provides a central location to organize and update thousands of leak records; authored a user manual for application

MANAGER, CAFE ZUPAS
NOVEMBER 2015 - JUNE 2018

- Managed 7-9 servers per shift while providing strong customer service skills
- Demonstrated strong leadership skills by training over fifty new hires

PROJECTS

- Designed a schematic and layout for a Flyback switching power supply controller chip using Cadence Virtuoso
- Designed a schematic and layout for a high speed, low power digital receiver circuit using Cadence Virtuoso
- Researched and presented a diagnosis for a patient in Atrial Ventricular Reentrant Tachycardia with a focus on how electrical signals travel throughout the heart
- Designed a low voltage operational amplifier with high DC open-loop gain (over 60-80 dB for various VDDs) that produces a quiescent current draw of less than 1 mA and a gain-bandwidth product of over 5.8MHz using LTspice
- Researched and presented on different photovoltaic modules with a focus on the results of bifacial PV modules
- Designed and implemented a traffic light on both a breadboard using LEDs and FPGA using VGA