# JAZMINE BOLOOR

Las Vegas, Nevada | Email: boloor@unlv.nevada.edu

## EDUCATION

**UNIVERSITY OF NEVADA, LAS VEGAS,** ELECTRICAL ENGINEERING
(AUGUST 2017 – PRESENT)

- Expected Graduation: May 2021

## SKILLS & ABILITIES

**SKILLS**: PCB layout, soldering, wire bonding, FPGA programming
**LANGUAGES:** C++, Verilog, VHDL, MATLAB
**SOFTWARE:** Cadence Virtuoso, LTspice, DipTrace, Eagle, PowerWorld, CAD, Quartus

## EXPERIENCE

**UNLV UNDERGRADUATE RESEARCH ASSISTANT**, DR. R JACOB BAKER
MARCH 2019 - PRESENT

- Designed and tested Printed Circuit Boards (PCBs) on Eagle and Diptrace; soldered surface-mount and through-hole components; wire bonded using K&S Ltd. Dicing Systems Wire Bonder (4526)
- Designed 3D printed models using CAD software

**STUDENT DIRECTOR**, LAS VEGAS SCHOLARS PROGRAM
AUGUST 2019 - PRESENT

- Planned monthly events to broaden career opportunities for students in STEM
- General point of contact and support for nine engineering students

**ENGINEERING INTERN,** SOUTHWEST GAS
MAY 2019 - AUGUST 2019

- Member of the Distribution Integrity Management Program team, where distribution pipeline leak analysis data is analyzed to optimize leak prevention
- Worked with a team of testers on the launch of a new application that provides a central location to organize and update thousands of leak records; authored a user manual for application

**MANAGER,** CAFE ZUPAS
NOVEMBER 2015 - JUNE 2018

- Managed 7-9 servers through entire shifts
- Demonstrated strong leadership skills by training over fifty new hires

## PROJECTS

- Designed a schematic and layout for a Flyback switching power supply controller chip using Cadence Virtuoso
- Designed a schematic and layout for a high speed, low power digital receiver circuit using Cadence Virtuoso
- Designed a low voltage operational amplifier with high DC open-loop gain (over 60-80 dB for various VDDs) that produces a quiescent current draw of less than 1 mA and a gain-bandwidth product of over 5.8MHz using LTspice
- Researched and presented a diagnosis for a patient in Atrial Ventricular Reentrant Tachycardia with a focus on how electrical signals travel throughout the heart
- Designed and implemented a traffic light on both a breadboard using LEDs and FPGA using VGA