

# Higher-order Modulators

November 2011

## 7.3 Noise-shaping topologies

1st & 2nd

Higher-order topologies

3rd DSM

$$NTF = (1+z^{-1})^3$$



Stability!

# M<sup>th</sup>-order modulator

filtering  
 $L = 1 + M$   
 order of sinc filter

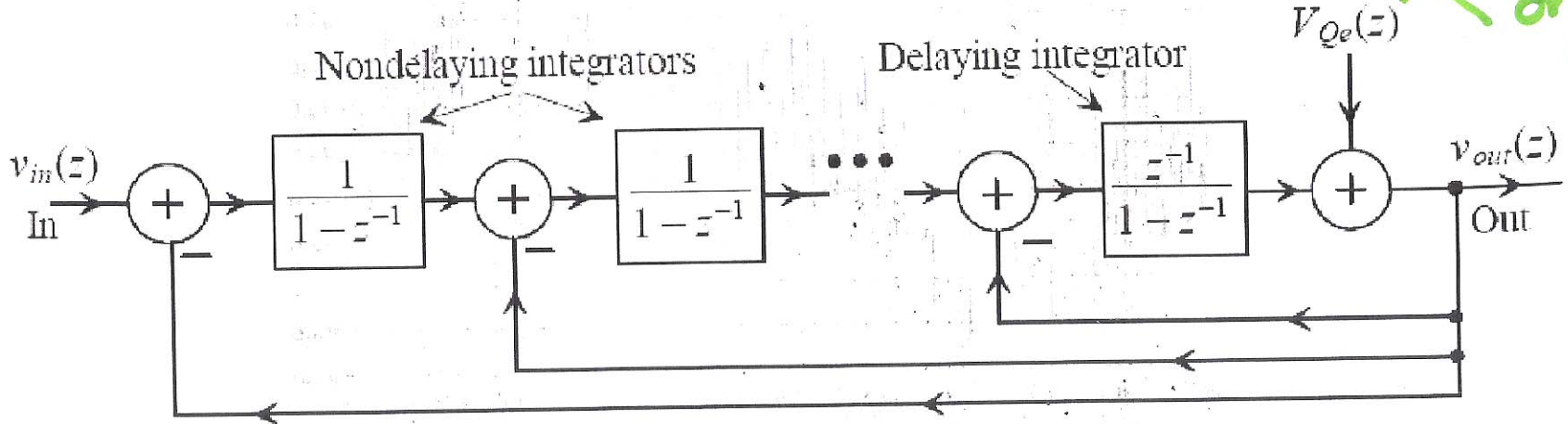
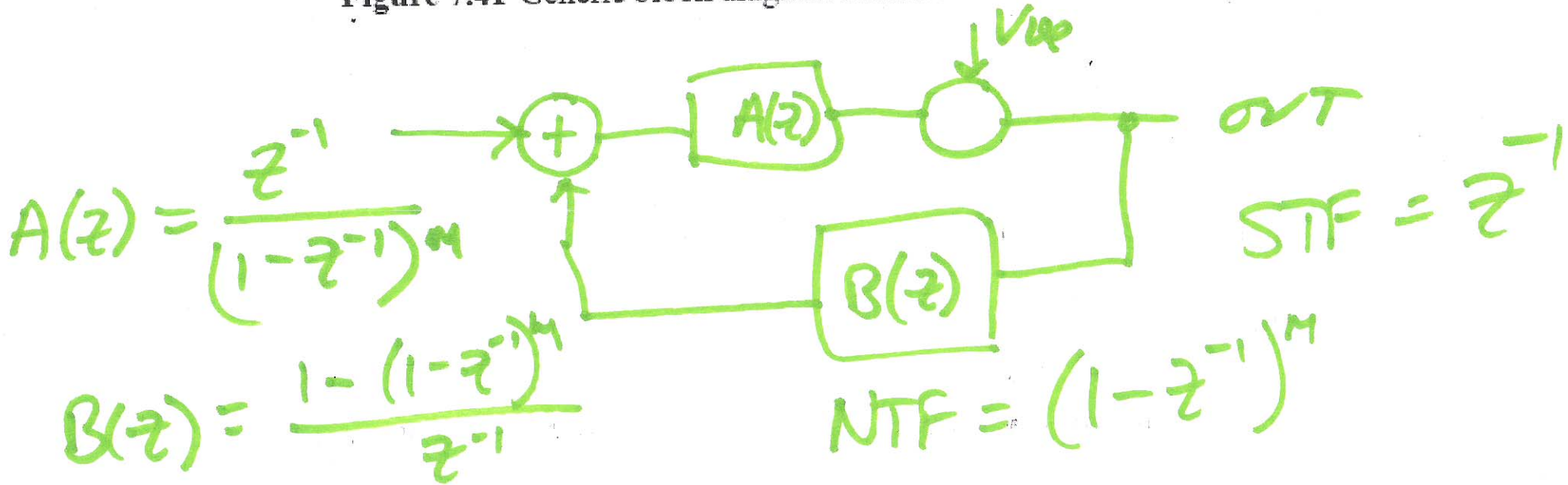


Figure 7.41 Generic block diagram of an M<sup>th</sup>-order NS modulator.



2)

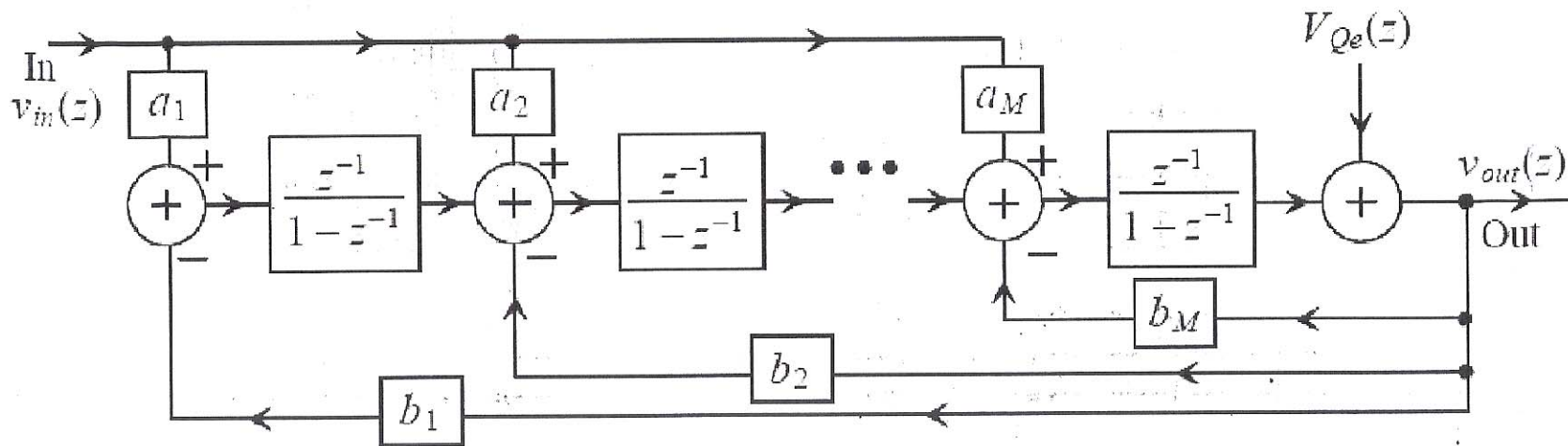
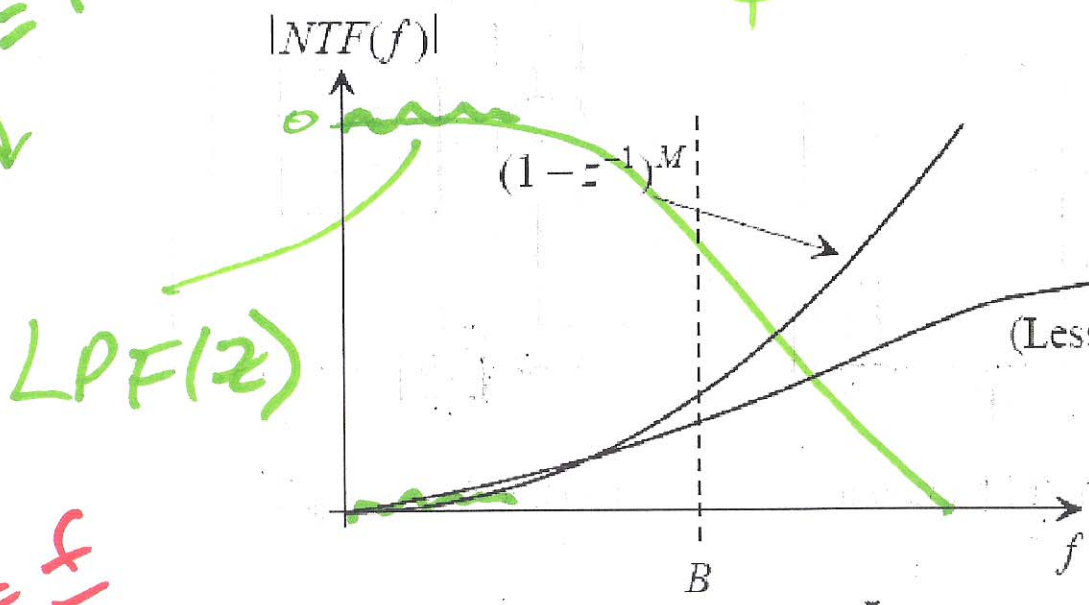
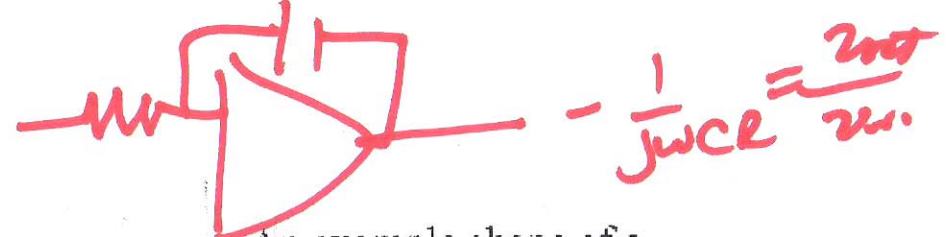


Figure 7.42 Block diagram of a modified  $M^{\text{th}}$ -order NS modulator.

3)

$$z = 1 + j\omega \frac{1}{f_s}$$



An example shape of a higher-order NTF.  
 $LPF(z) \cdot (1 - z^{-1})^M$   
 (Less gain at high frequencies)

$$RC = \frac{f}{f_s}$$

Figure 7.43 Showing the change in the NTF in a higher-order modulator.

$$\frac{1}{z-1} = \frac{z^{-1}}{(1-z^{-1})^M}, \quad A(z) = \sum_{i=1}^M a_i \left( \frac{z^{-1}}{1-z^{-1}} \right)^{M-i+1}$$

$$\frac{z^{-1}}{1-z^{-1}} \approx \frac{1}{j\omega RC} = \left( \frac{z^{-1}}{1-z^{-1}} \right)^M + \sum_{i=2}^M a_i \left( \frac{z^{-1}}{1-z^{-1}} \right)^{M-i+1}$$

$$z^{-1} = e^{-j2\pi f \frac{1}{f_s}} \approx 1 + j \left( -\frac{2\pi f}{f_s} \right)$$

4)



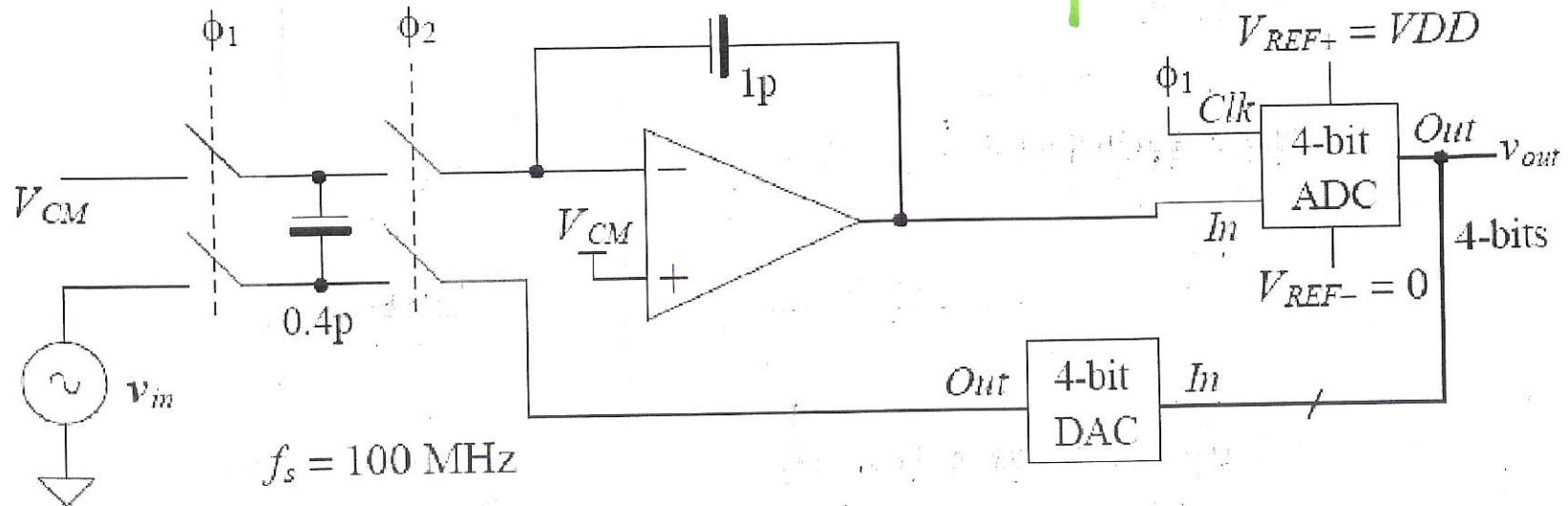
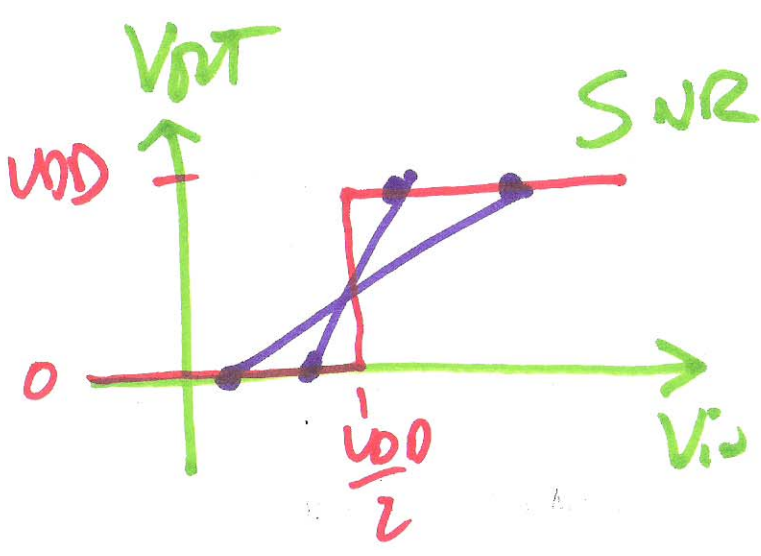


Figure 7.44 Circuit implementation of a first-order multi-bit NS modulator.



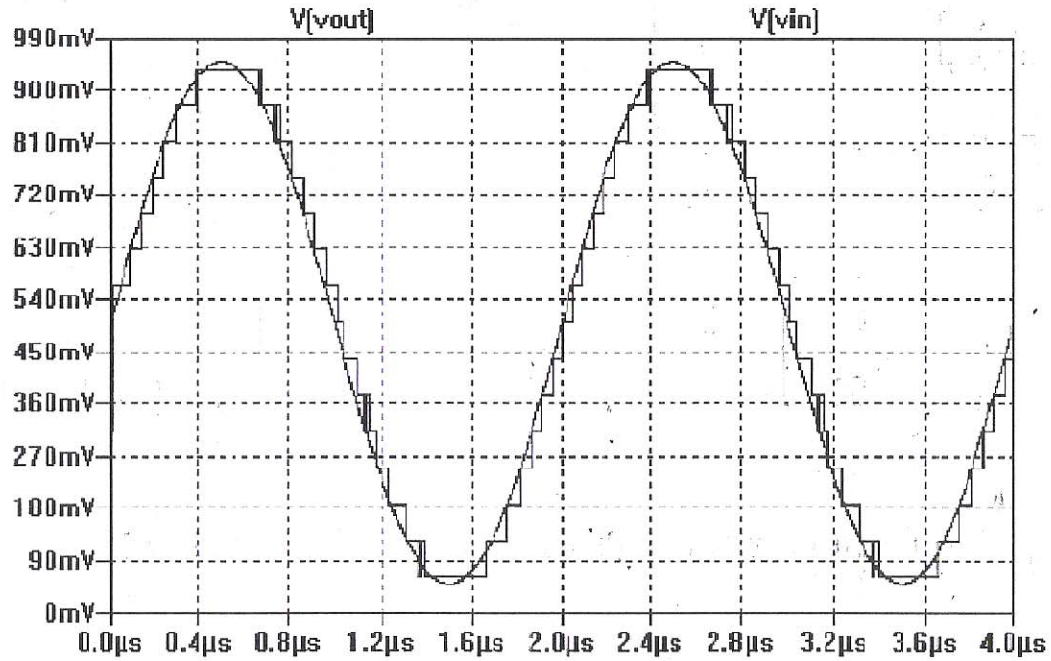
$$SNR = 6.02(N + N_{inc})$$

# of bits in the quantizer  
 increase due to oversampling

5)

LSB = 104 A

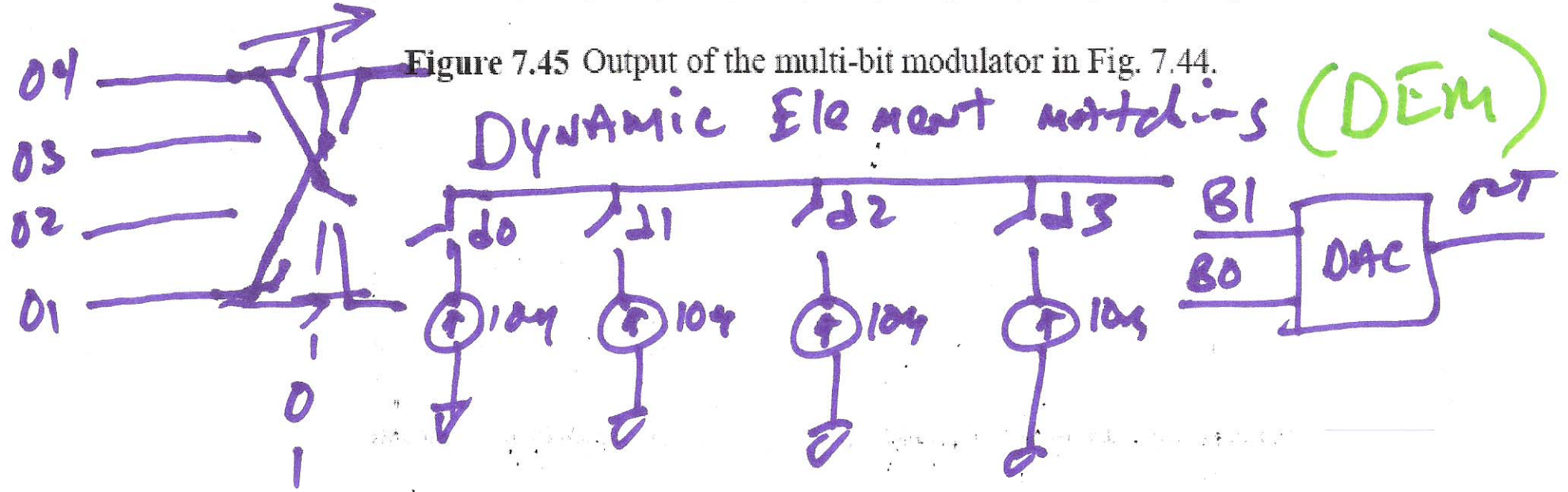
B1	B0	OUT
0	0	104
0	1	204
1	0	304
1	1	404



VARIANCE =  $\frac{K^2}{N}$

Figure 7.45 Output of the multi-bit modulator in Fig. 7.44.

DYNAMIC ELEMENT MATCHING (DEM)



6)



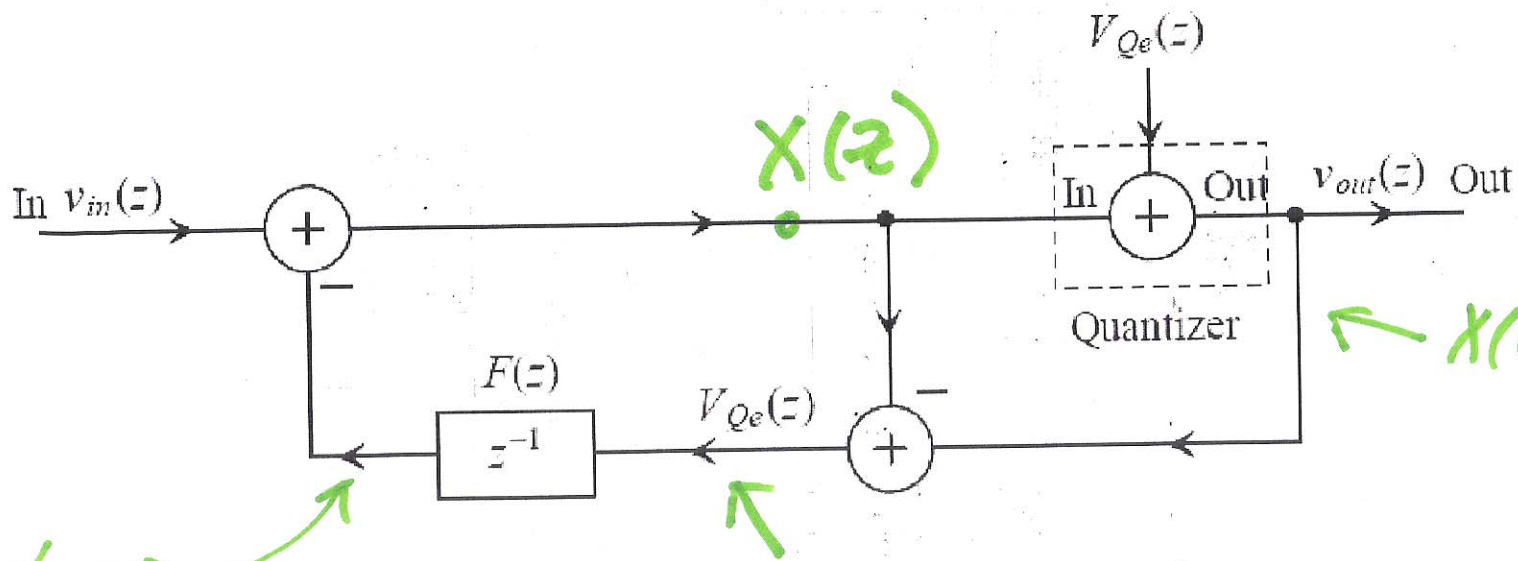


Figure 7.47 Block diagram of an error feedback modulator.

$$X(z) = v_{in}(z) - V_{Qe}(z) \cdot z^{-1}$$

$$v_{out}(z) = V_{Qe}(z) + v_{in}(z) - V_{Qe}(z) \cdot z^{-1}$$

$$v_{out}(z) = v_{in}(z) + V_{Qe}(z) (1 - z^{-1})$$



10-bits

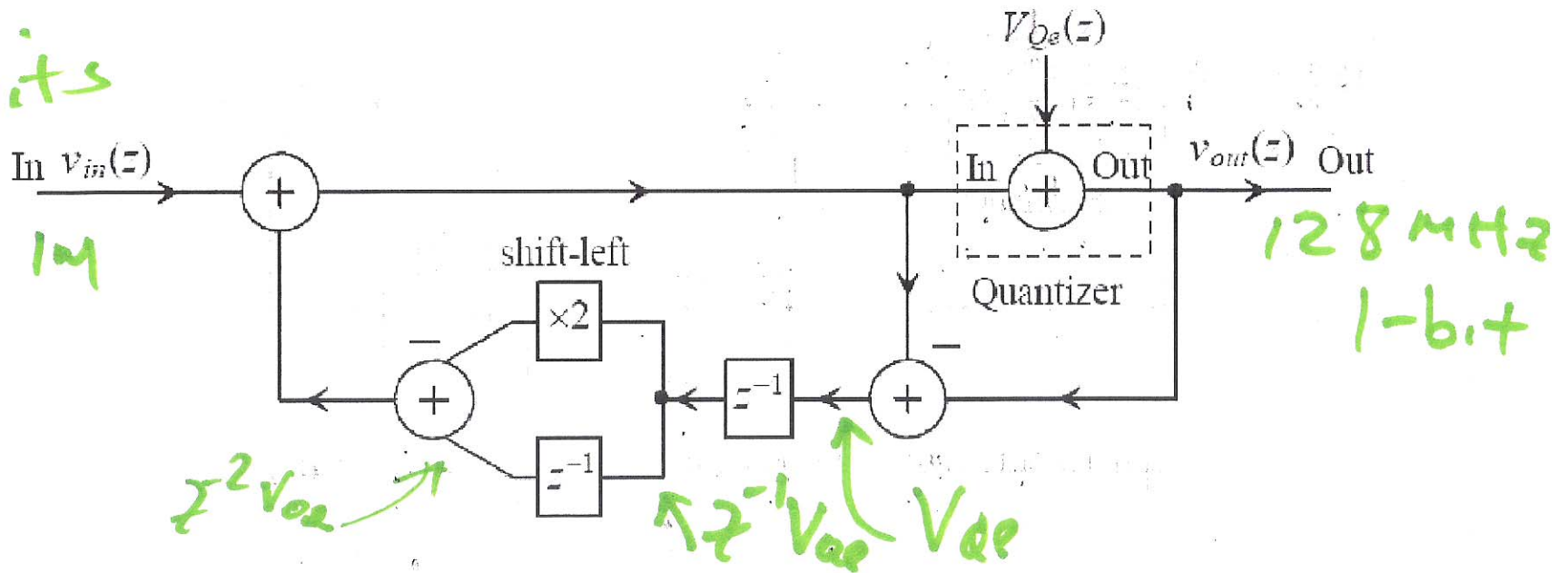


Figure 7.48 Block diagram of a second-order error feedback modulator.

$$z^{-2}V_{qe}(z) + 2z^{-1}V_{qe}(z) + V_{in}(z) + V_{qe}(z) = V_{out}$$

$$V_{out} = V_{in}(z) + V_{qe}(z)(1 - 2z^{-1} + z^{-2})$$

$$= V_{in}(z) + V_{qe}(z)(1 - z^{-1})^2$$

9)

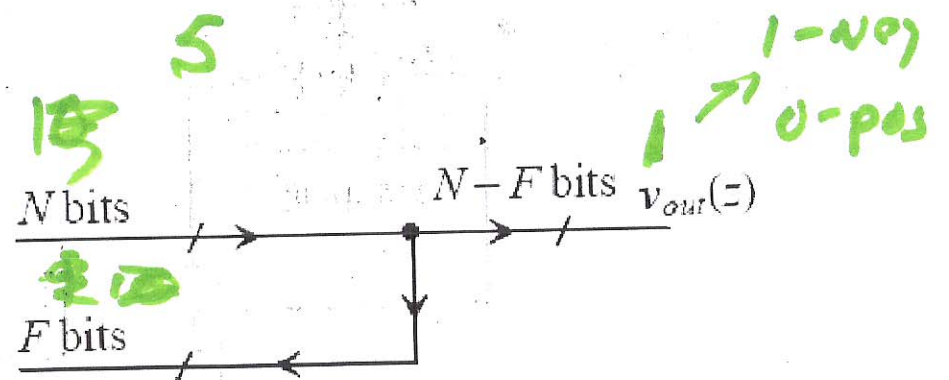
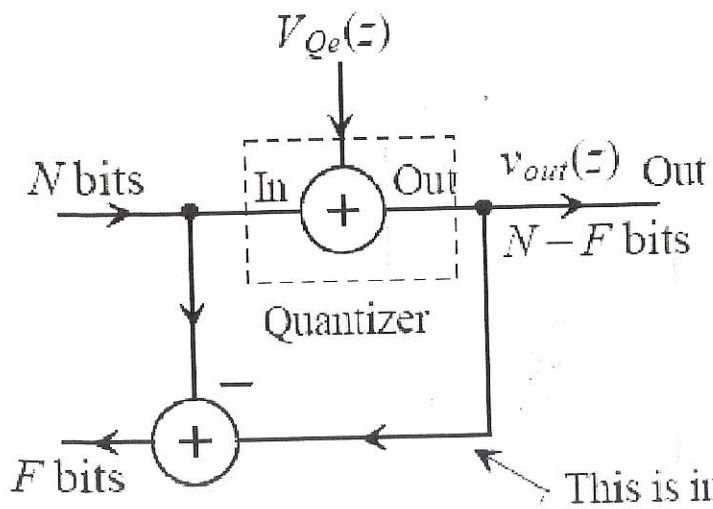


Figure 7.49 Showing how the quantizer and difference block are implemented.

0111 → 7  
 01010 → +10 - 8  
 0010 → 2  
 0001 → 1  
 0000 → 0  
 1111 → -1  
 1110 → -2  
 1000 → -8  
 00010 → +2  
 1110 → -3  
 → 4

10)

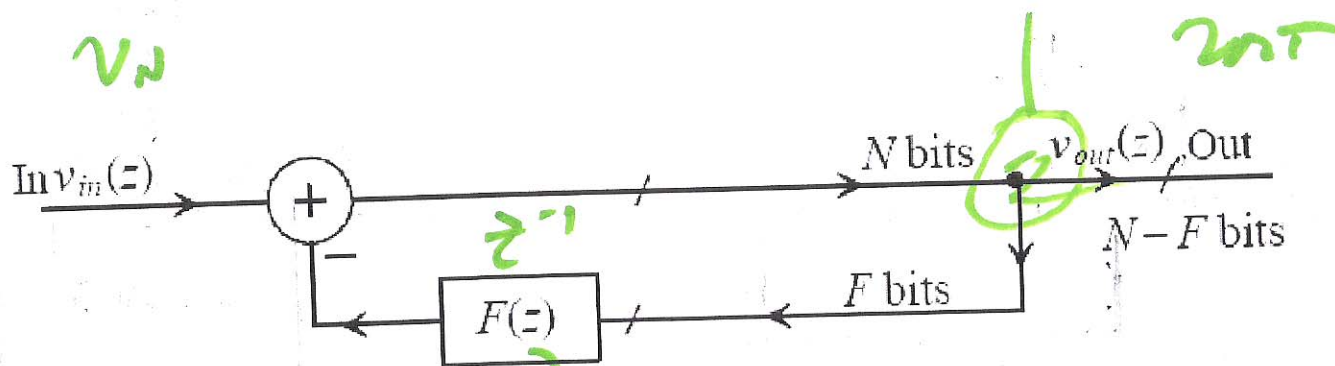


Figure 7.50 Block diagram of an error feedback modulator.

$$F(z) = z^{-1} \text{ for first-order}$$

$$v_N - z^{-1} v_{NT} = v_{NT}$$

$$v_N = v_{NT} (1 + z^{-1})$$

$$\frac{v_{NT}}{v_N} = \frac{1}{1 + z^{-1}}$$

ii)

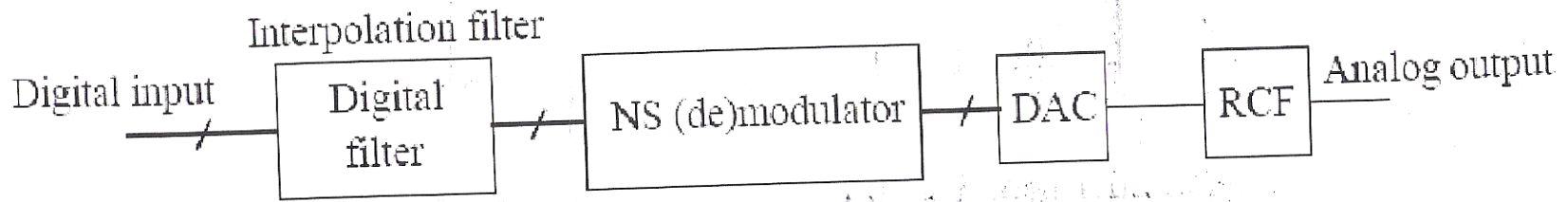


Figure 7.51 DAC using a NS modulator and digital filter.