

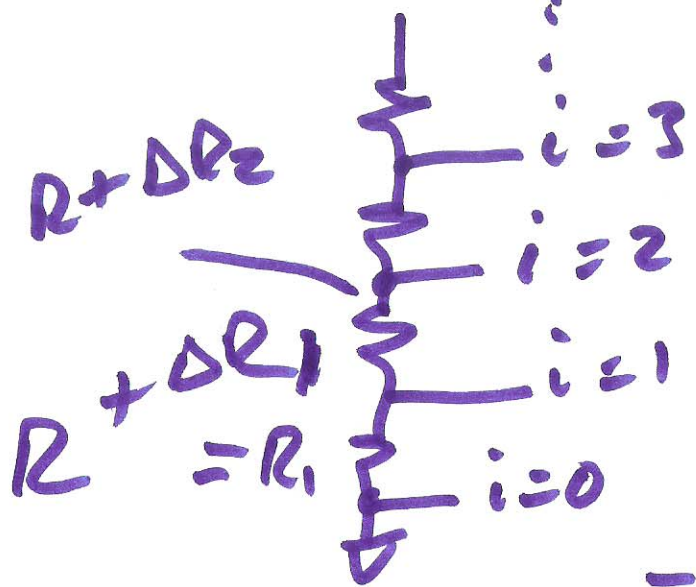
29.1 DACS

Mismatch $\rightarrow \frac{\Delta R}{R_{avg}} = 0.01, 1\% \text{ mismatch}$

$$\frac{\Delta R}{R_{avg}} = \frac{\Delta R}{R} \quad R = R_{avg} \left(1 + \frac{\Delta R}{R_{avg}} \right)$$

$$R = R_{avg} + \Delta R$$

$i = 2^N - 1$



$$R = \sum_{i=1}^{2^N} (\Delta R_i + R)$$

$$-10 < \Delta R_i < 10$$

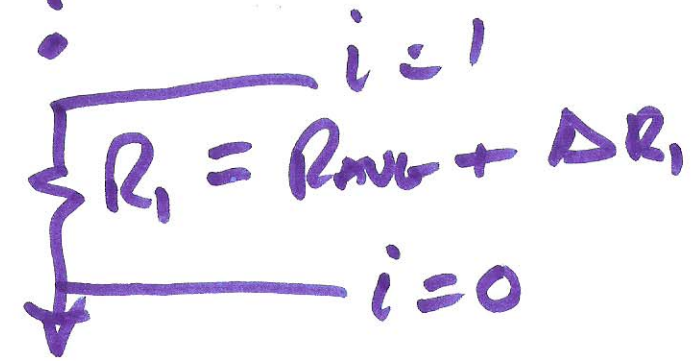
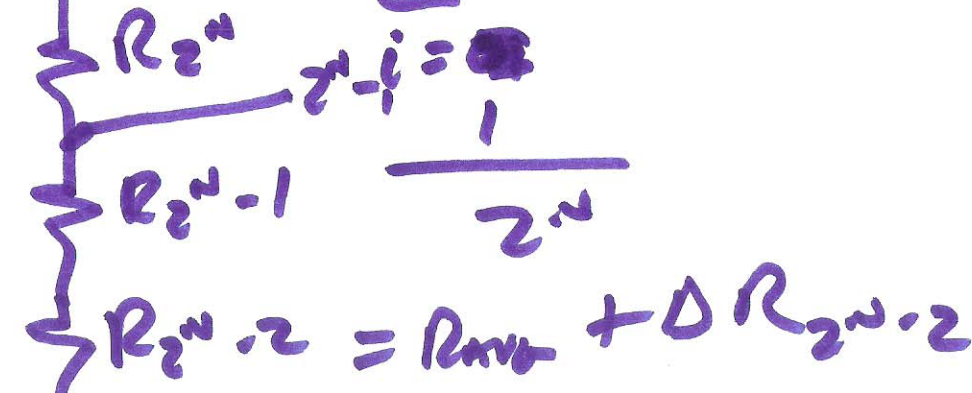
↑
RANDOM VALUE

2^N ONLY if

$$\sum_{i=1}^{2^N} \Delta R_i = 0$$

V_{REF}

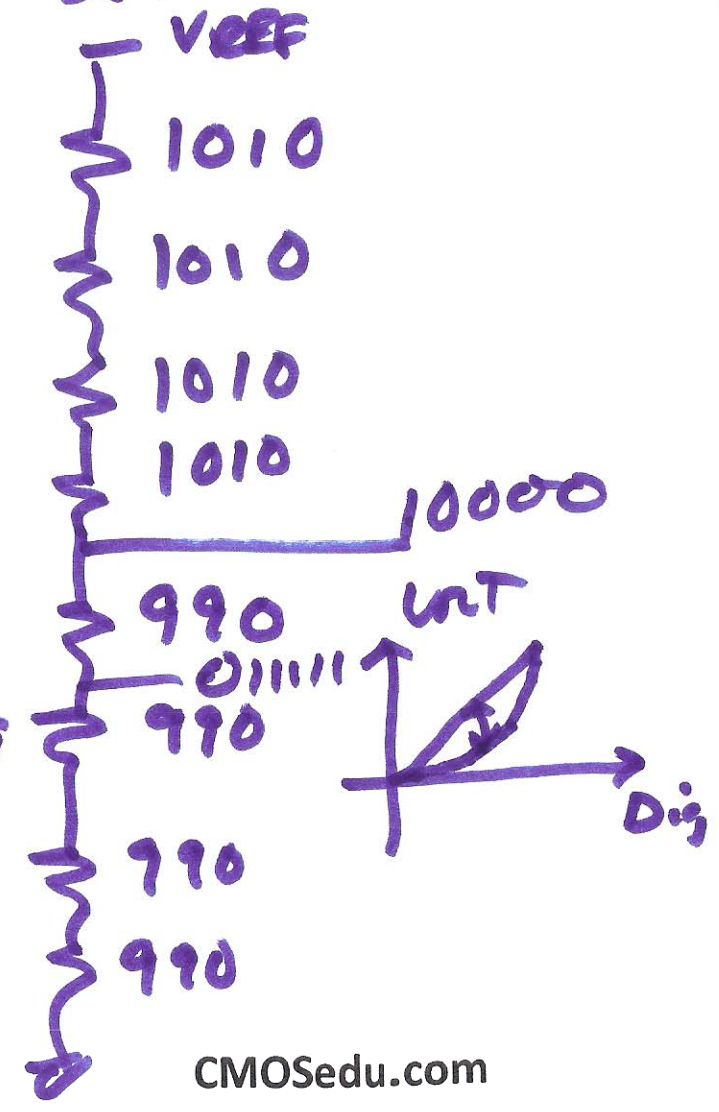
$$\sum_{i=0}^{2^N} R_i = R = R_{AVG}$$



$$V_{LSB} = \frac{V_{REF}}{2^N}$$

ideal

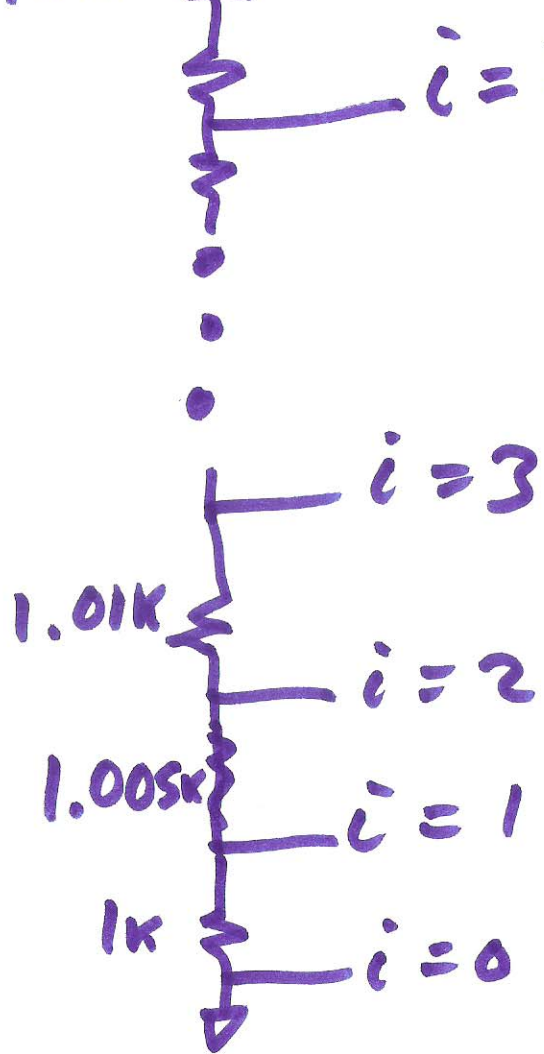
Sec. 29.1



2)

A29.1

VRE



$$i = 2^n - 1 = 1023$$

How many resistors?
 $1024 = 2^{10}$

$$R = \sum_{j=0}^{1023} (1k + 5j)$$

$$= \sum_{j=1}^{1024} (1k + 5(j-1))$$

$$1 + 2 + 3 + 4 + 5 + 6 + \dots + 1024 = \frac{1024(1024+1)}{2} \approx \frac{1024^2}{2} = \frac{1023^2}{2} + 1024$$

3)

$$R = \frac{1024 \cdot 1k + 5 \cdot \frac{1023 \cdot 1024}{2}}{1024}$$

$$= 1k + 2.5 \cdot 1023$$

$$R = 3557.5 \Omega$$

sum
i=1

$$V_{i, \text{actual}} = V_{REF} \cdot \frac{\sum_{j=1}^i (1k + (j-1)5)}{2^N \cdot 3557.5}$$

$$V_{i, \text{ideal}} = \frac{V_{REF}}{2^N} \cdot i$$

4)

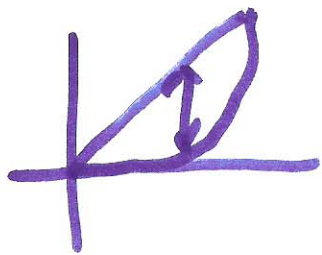
$$\frac{DR}{R} = R_{NL} + V_{i, ideal} = \frac{V_{REF}}{2^N} \cdot i$$

$$V_{i, actual} = \frac{V_{REF}}{2^N} \cdot \sum_{j=0}^i \frac{(1K + (j-1)5)}{3557.5}$$

$$INL = V_{i, actual} - V_{i, ideal}$$

$$= \frac{V_{REF}}{2^N} \left(i - \sum_{j=0}^i \frac{1K + (j-1)5}{3557.5} \right)$$

WORST CASE INL @ $i = 512$



$$= 1 \text{ LSB} \left(512 - \frac{1}{3557.5} \left(512K + \underbrace{1 + 2 + 3 + \dots}_{\frac{j-1}{2}} \right) \right)$$

$$= \frac{K(K+1)}{2} = \frac{(j-1)(i)}{2}$$

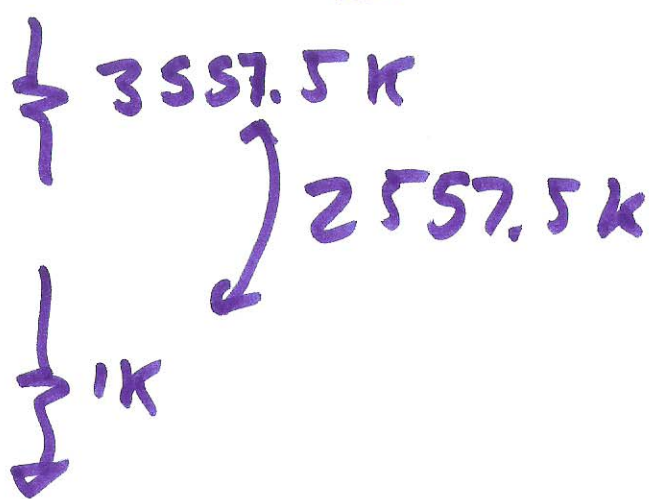
327
CMOSedu.com
175 LSB!

$$R_i = R_{avg} \left(1 + \frac{\Delta R}{R} \right)$$

$$1k = 3557.5 \left(1 + \frac{\Delta R}{R} \right)$$

$$\left. \begin{array}{l} \text{UNCL} \\ \{ 3557.5 + 2557.5k \approx 6.1k \end{array} \right\}$$

$$\frac{\Delta R}{R} = \frac{1k}{3557.5} - 1 = -0.72$$



MATCH 72%!

DNL

$$V_{i, \text{Actual}} - V_{i-1, \text{Actual}}$$

b)

$$DNL \approx \frac{V_{REF}}{2^N} \left(\sum_{j=1}^i \frac{1k + (j-1)5}{3557.5} - \sum_{j=1}^{i-1} \frac{1k + (j-1)5}{3557.5} \right)$$

\nearrow
 1LSB

$$= 1LSB \left(\frac{1k + 5(i-1)}{3557.5} \right)$$

$$DNL \approx \frac{1}{3} LSB \quad i=1$$

$$DNL \approx -\frac{1}{2} LSB \quad i=512$$

$$DNL \approx -1.2 LSB \quad i=1024$$

7)

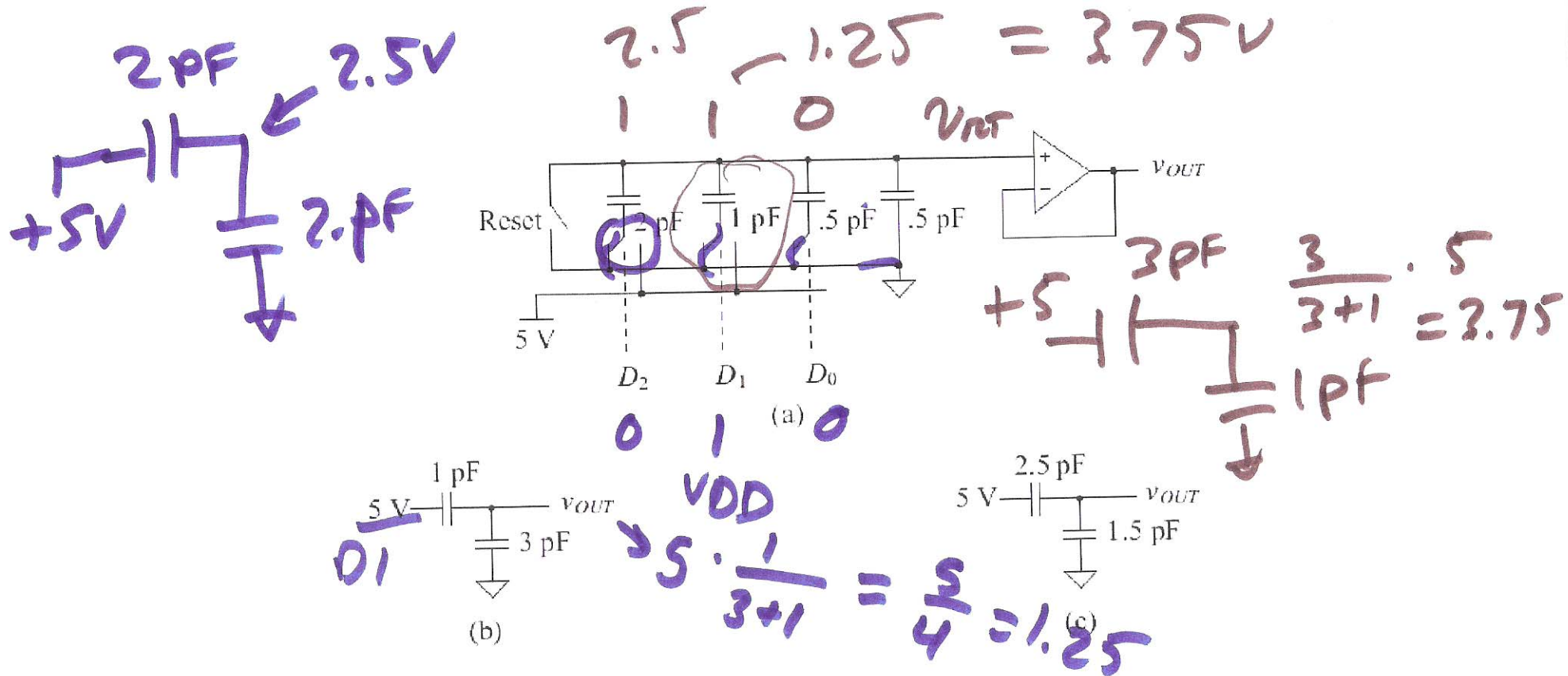
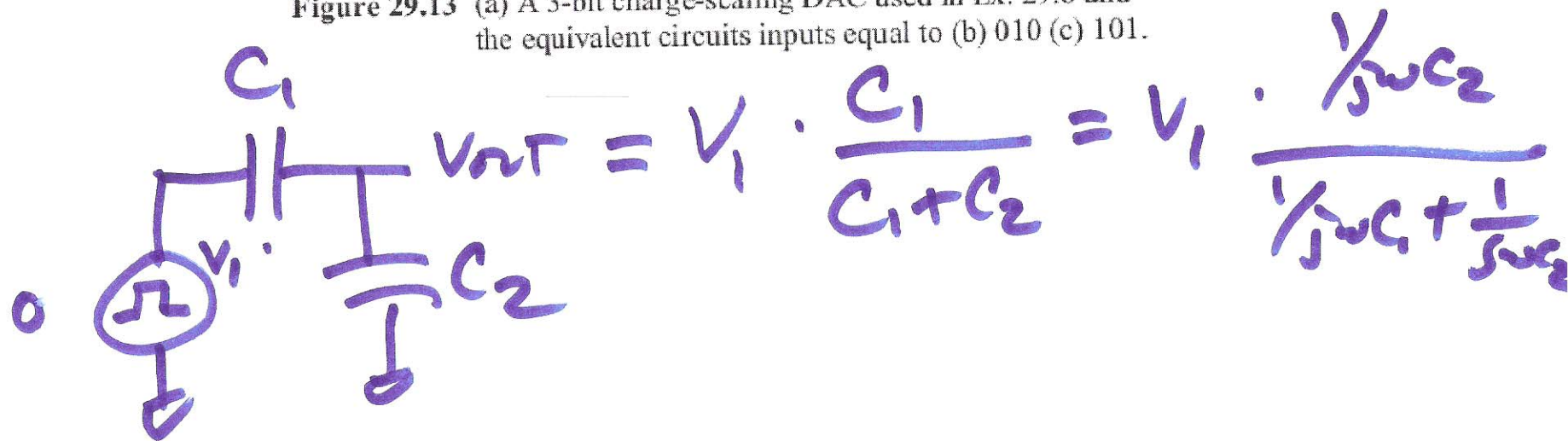


Figure 29.13 (a) A 3-bit charge-scaling DAC used in Ex. 29.6 and the equivalent circuits inputs equal to (b) 010 (c) 101.



8)

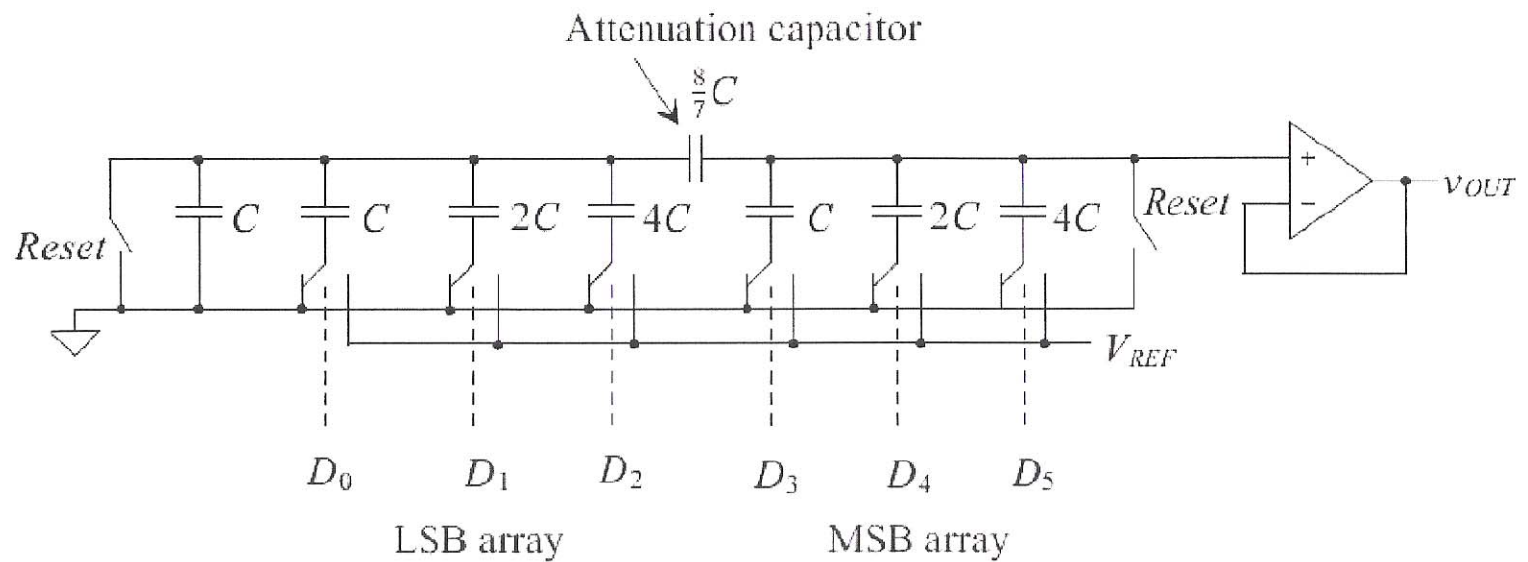
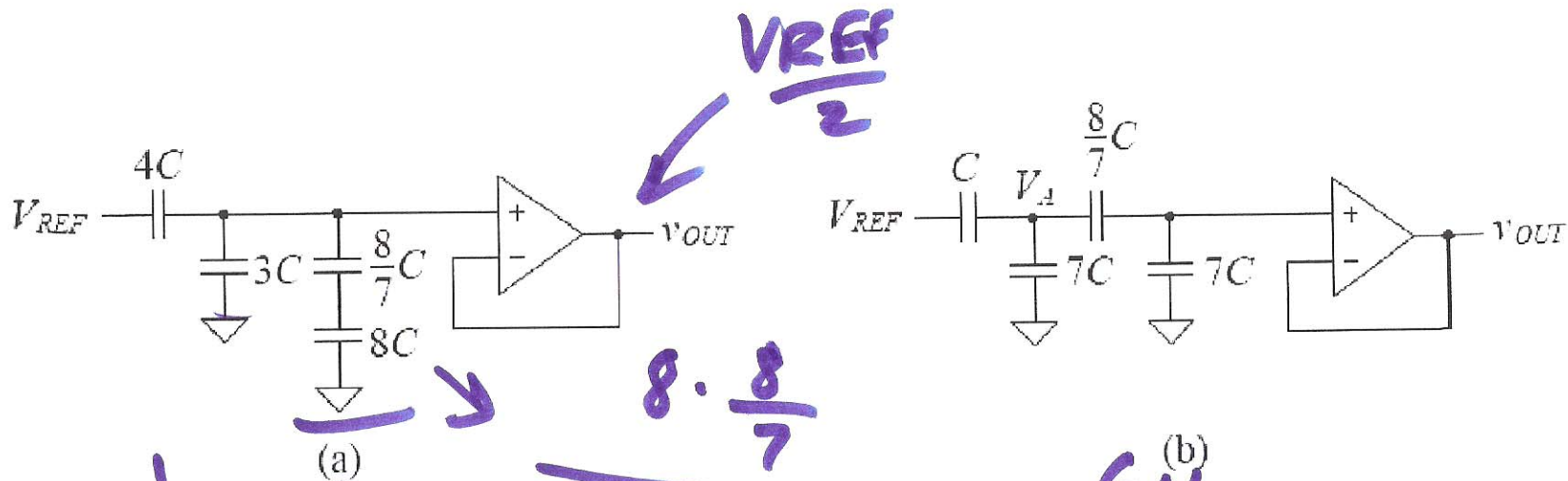


Figure 29.15 A charge-scaling DAC using a split array.





(a) $\rightarrow \frac{8 \cdot \frac{8}{7}}{8 + 8} = \frac{64}{56 + 8} = 1$ (b)

Figure 29.16 Equivalent circuits for Example 29.7.

Look at 29.7

10)

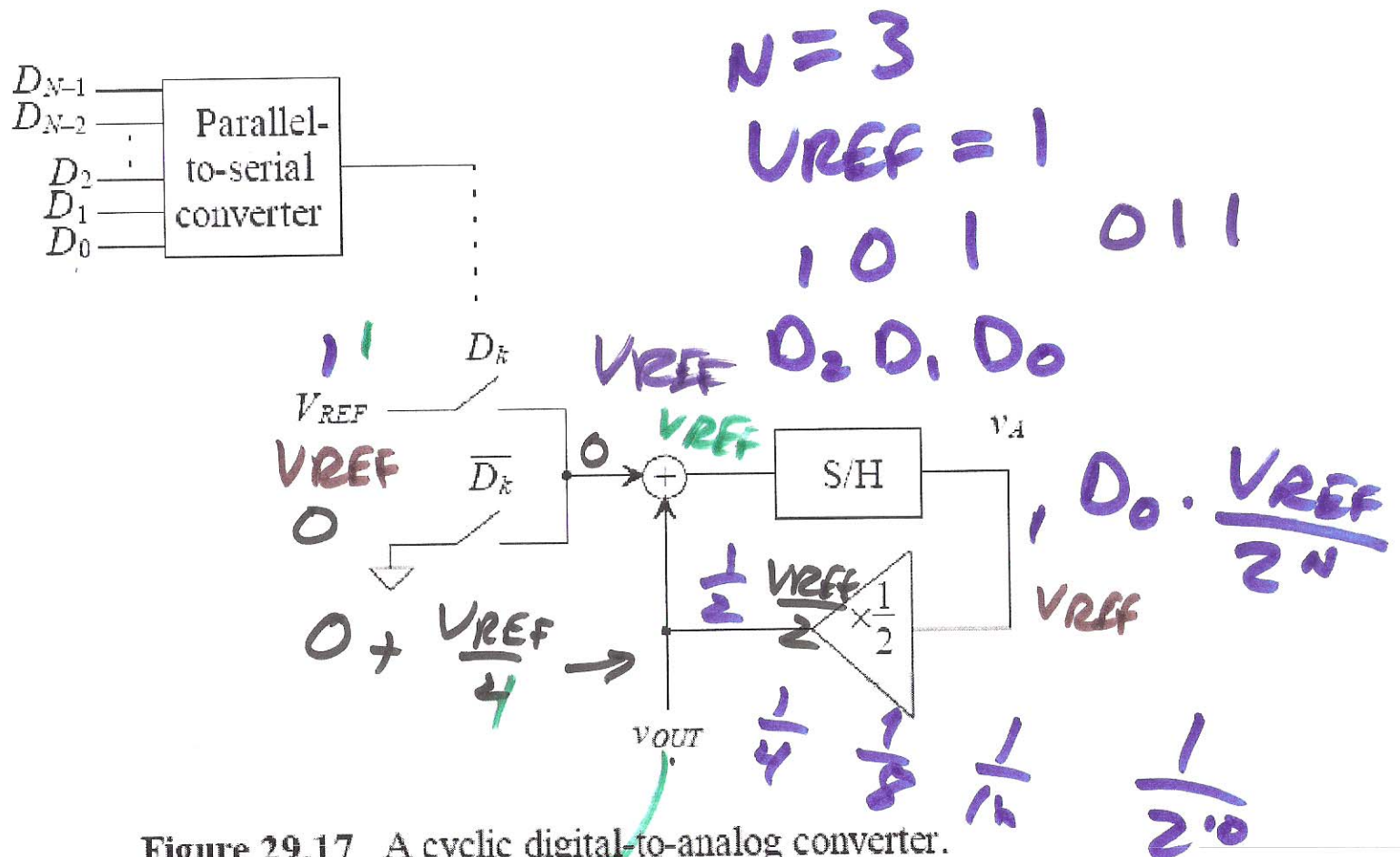


Figure 29.17 A cyclic digital-to-analog converter.

$$0 + \frac{V_{REF}}{2^4} + \frac{V_{REF}}{8}$$

$$\frac{V_{REF}}{2} + 0 + \frac{V_{REF}}{8}$$

11)

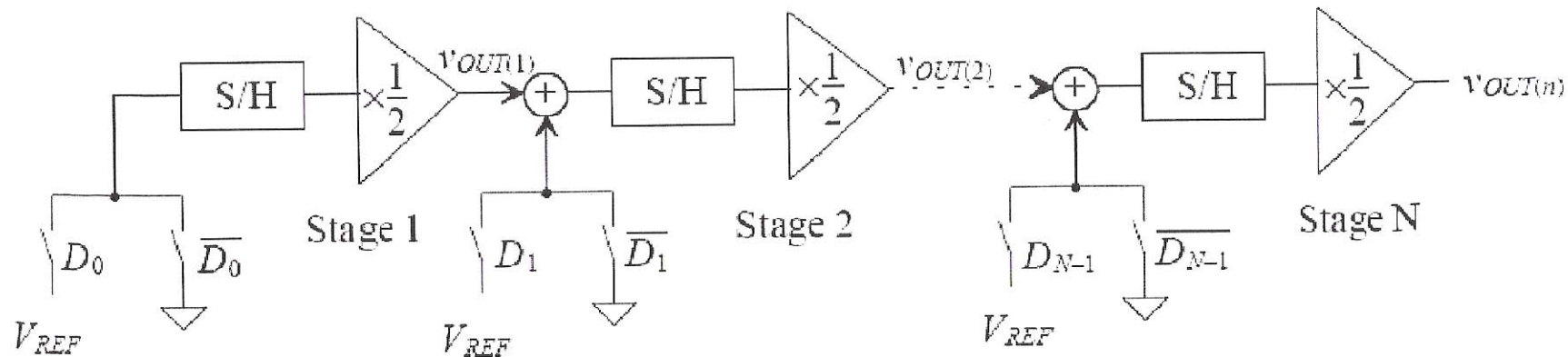


Figure 29.19 A pipeline digital-to-analog converter.

$\leftarrow 2^{n-1} \text{LSBS}$

$$D_{N-1} \cdot \frac{V_{REF}}{2} + D_{N-2} \cdot \frac{V_{REF}}{4} + \dots$$

$$D_1 \cdot \frac{V_{REF}}{2^{n-1}} \cdot \frac{2}{2} + D_0 \cdot \frac{V_{REF}}{2^n}$$

$\underbrace{\hspace{10em}}_{2 \text{LSB}} \qquad \underbrace{\hspace{10em}}_{1 \text{LSB}}$

(2)