

Chapter 28 - Data Converter Fundamentals

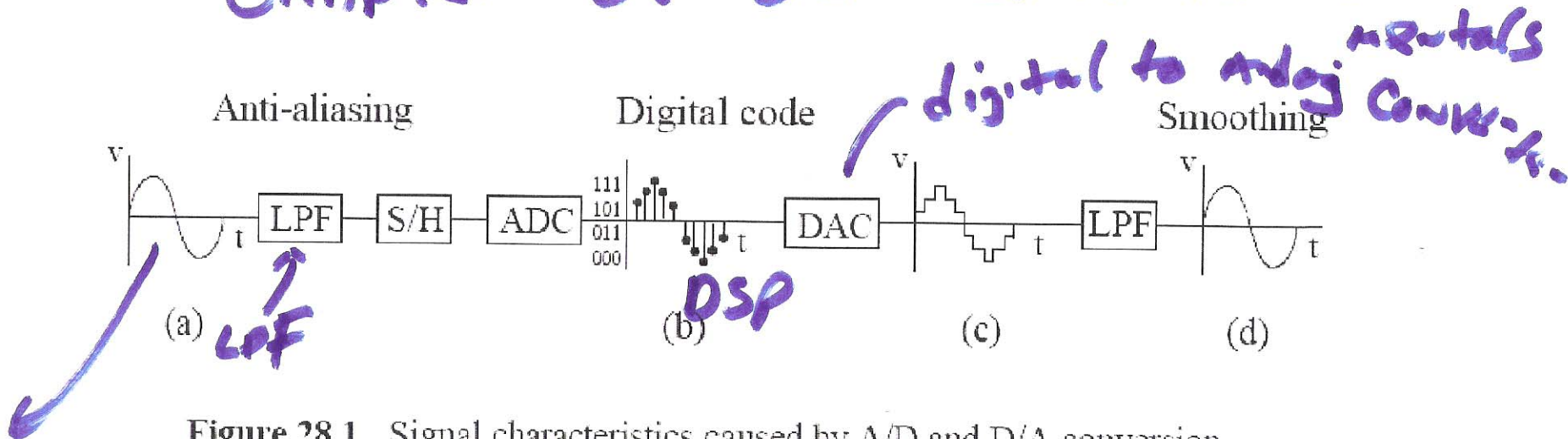


Figure 28.1 Signal characteristics caused by A/D and D/A conversion.

A small graph showing a vertical axis and a horizontal axis. A point on the horizontal axis is labeled f_s . An arrow points upwards from this point to a tick mark on the vertical axis.

$$1 \text{ LSB} = \frac{V_{REF+} - V_{REF-}}{2^N}$$

N = Number of bits in data converter

DR - dynamic range

ENOB - effective # of bits

DNL - Differential nonlinearity

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INL - Integral nonlinearity

1)

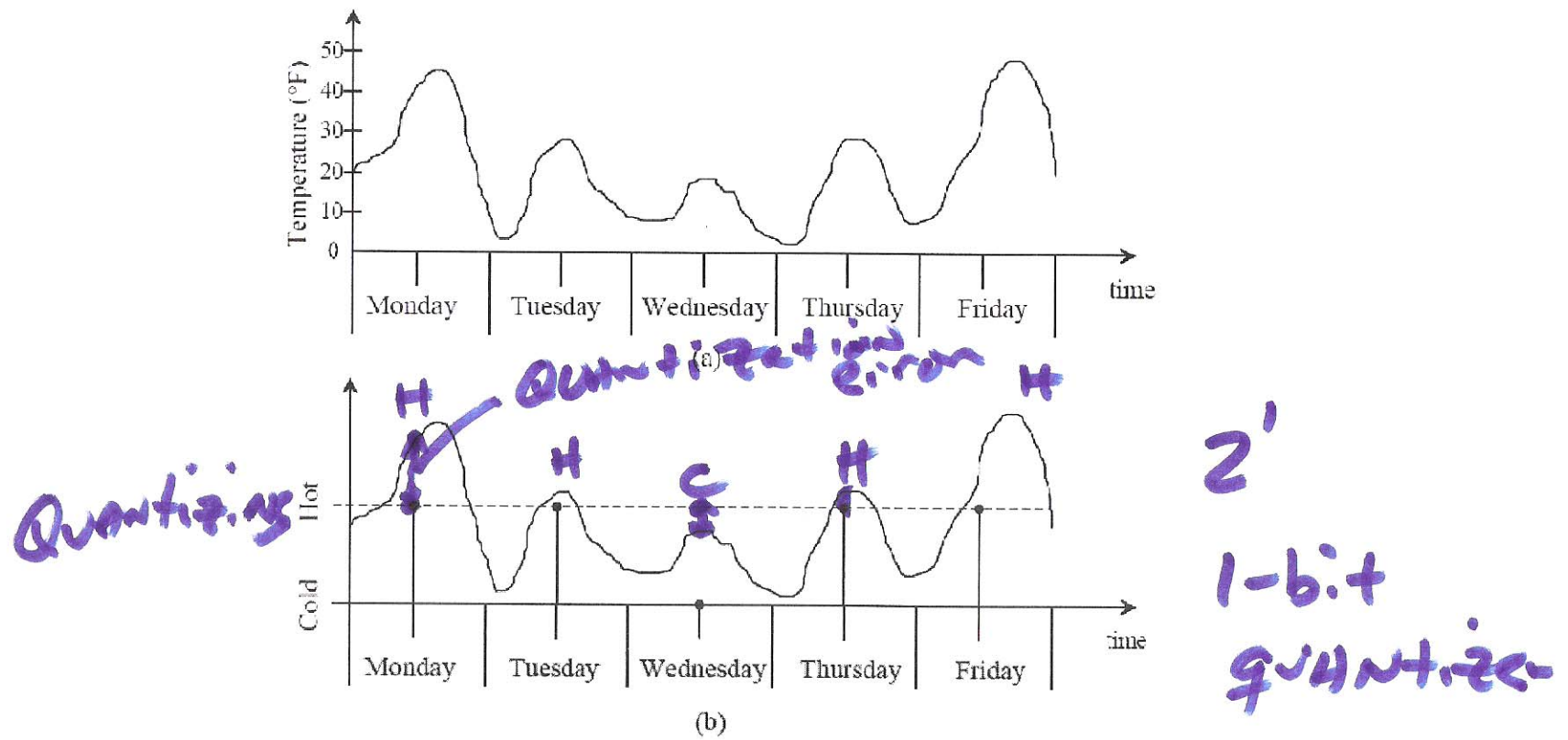
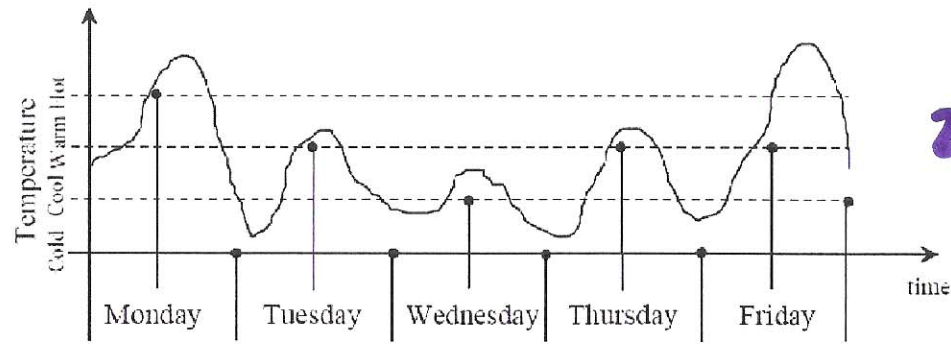


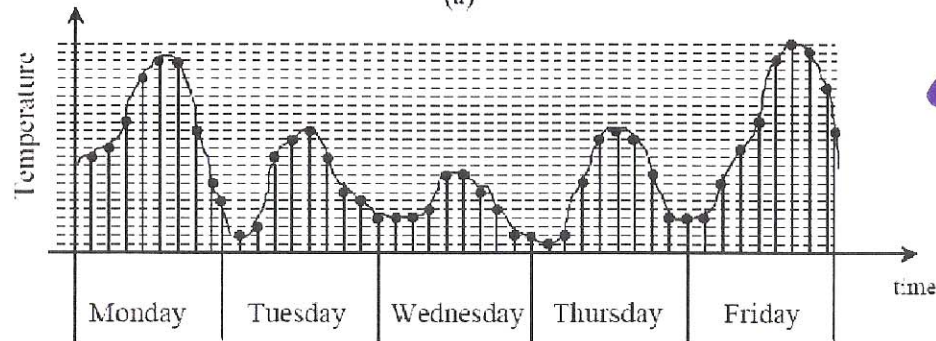
Figure 28.2 (a) An analog signal representing the temperature where you live and (b) a digital representation of the analog signal taking one sample per day with two quantization levels.

2)



2-bit quantization
or
ADC

(a)



4-bit ADC

(b)

Figure 28.3 Digital representation of the temperature taking (a) two samples per day with four quantization levels and (b) nine samples per day with 25 quantization levels.

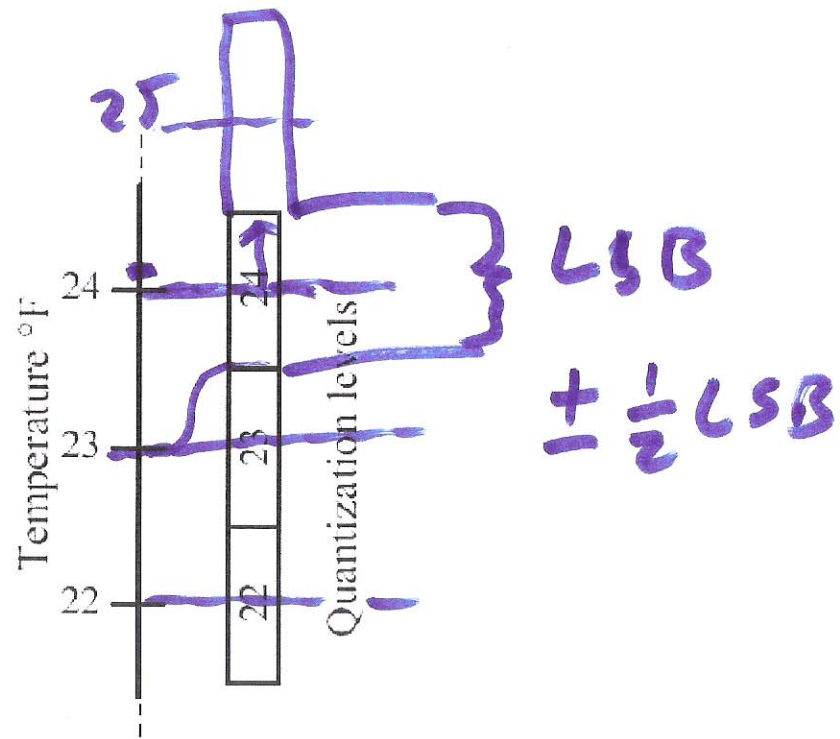


Figure 28.4 Quantization levels overlap actual temperature by $\pm \frac{1}{2}^{\circ}$ F.

4)

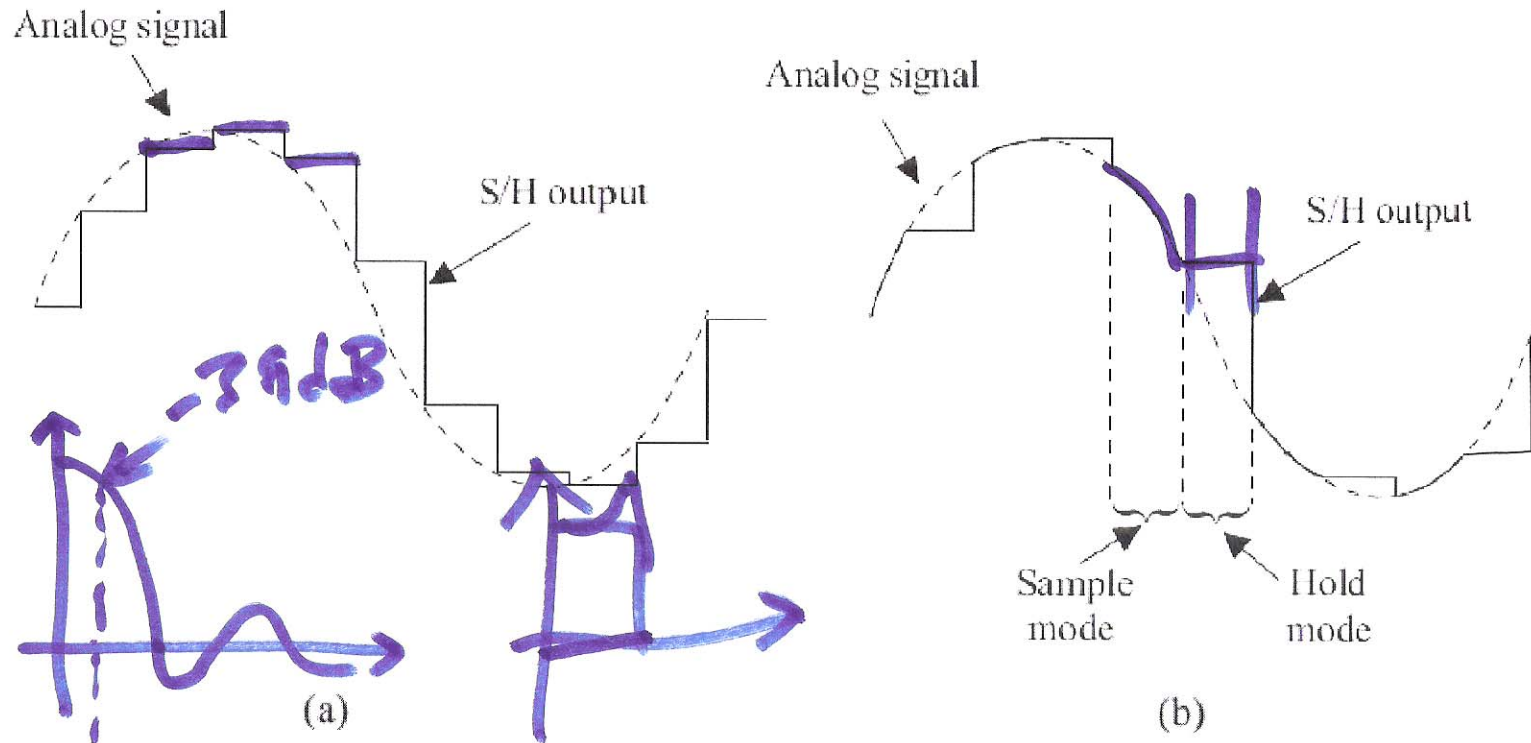


Figure 28.5 The output of (a) an ideal S/H circuit and (b) a track-and-hold (T/H).

$$\frac{f_s}{2} = \text{Nyquist frequency}$$

$$f_s = \text{sample frequency}$$

5)

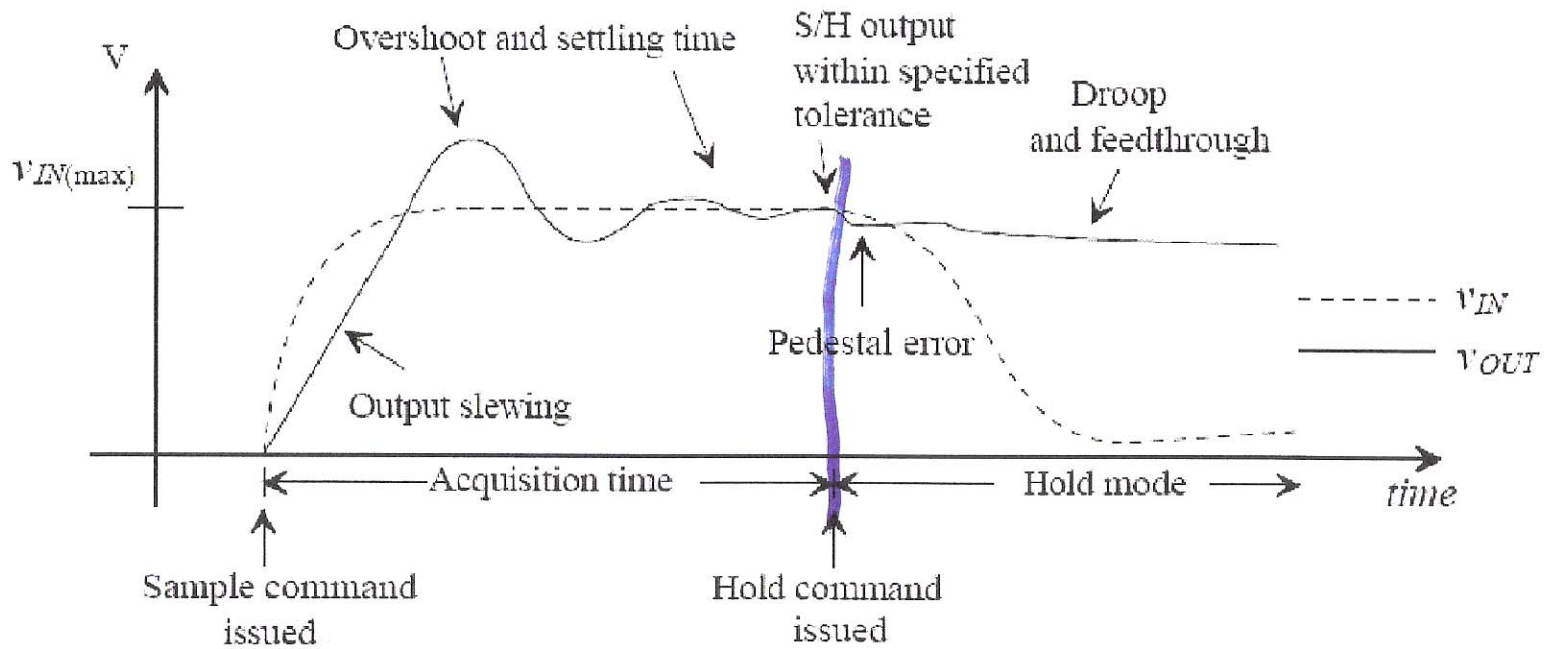
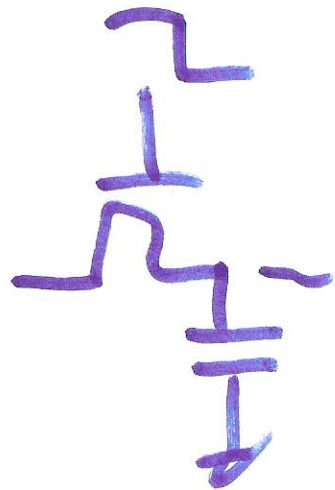


Figure 28.6 Typical errors associated with an S/H.



Characterizing

6)

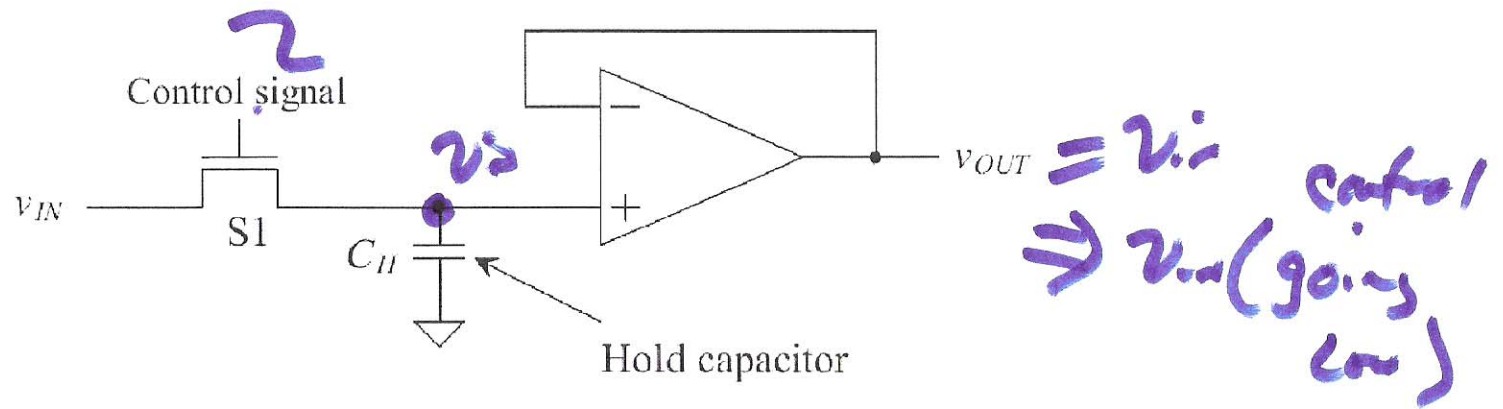


Figure 28.7 Track-and-hold circuit using an output buffer.

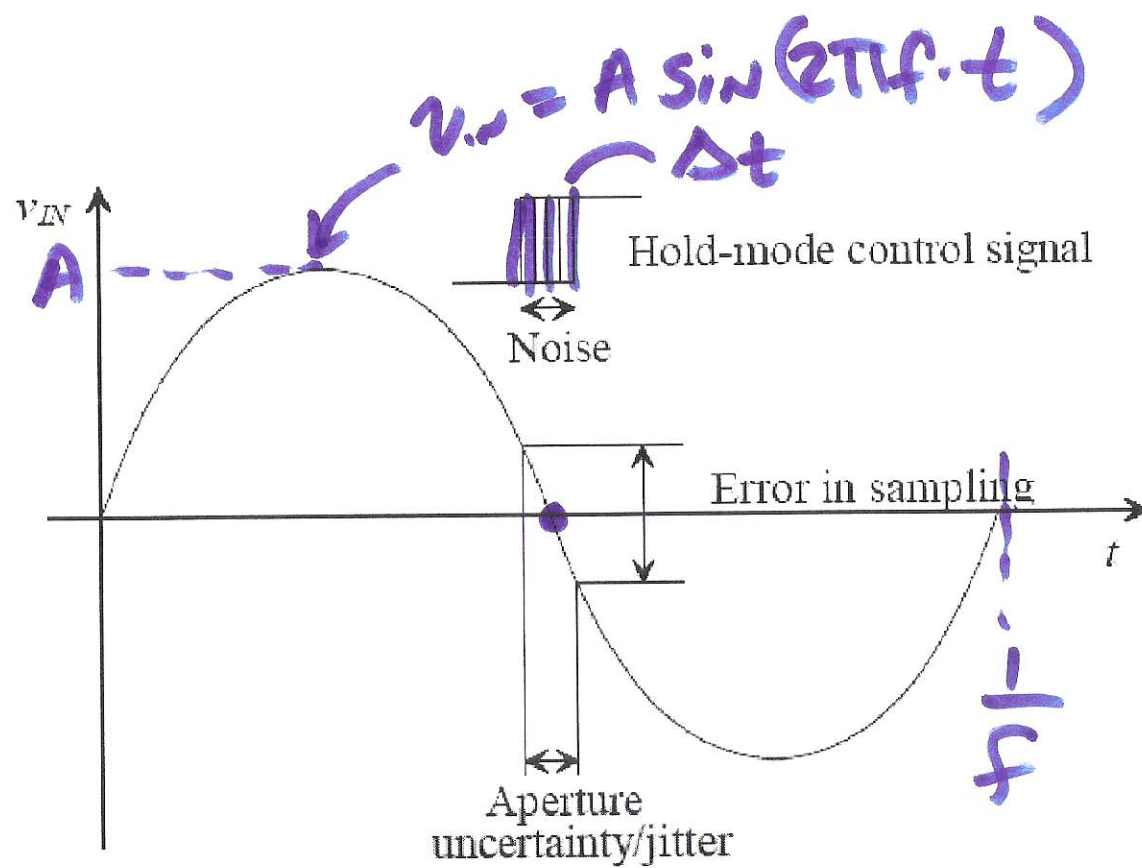


Figure 28.8 Aperture error.

$$\frac{dv_{in}}{dt} = A \cdot 2\pi f \cdot \cos 2\pi f t \cdot \frac{dt}{f t}$$

ERROR

$$A \cdot 2\pi f = \overline{\Delta t} \quad \text{ERROR} = 1 \cdot 2\pi \cdot 10^9 \cdot 10^{-11} = 62.8 \text{ mV}$$

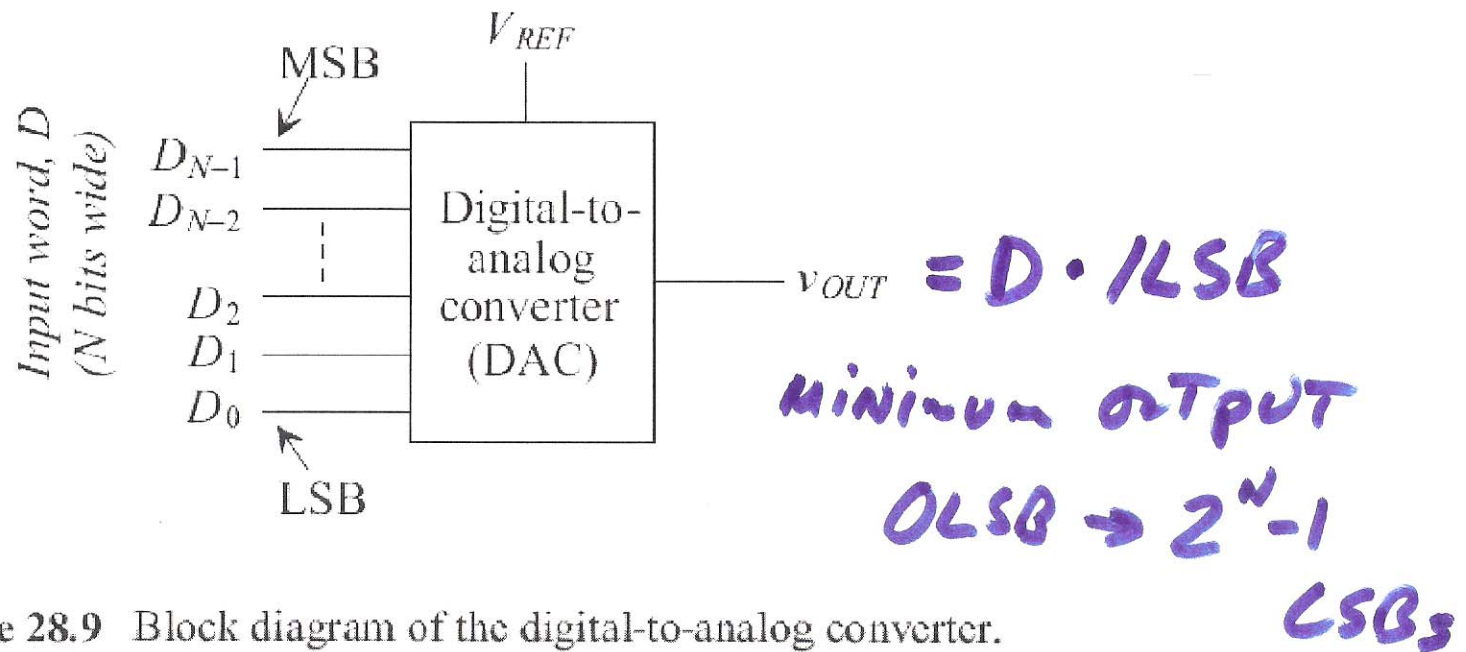


Figure 28.9 Block diagram of the digital-to-analog converter.

$$\text{F.S.} = 2^N - 1 \text{ LSBs}$$

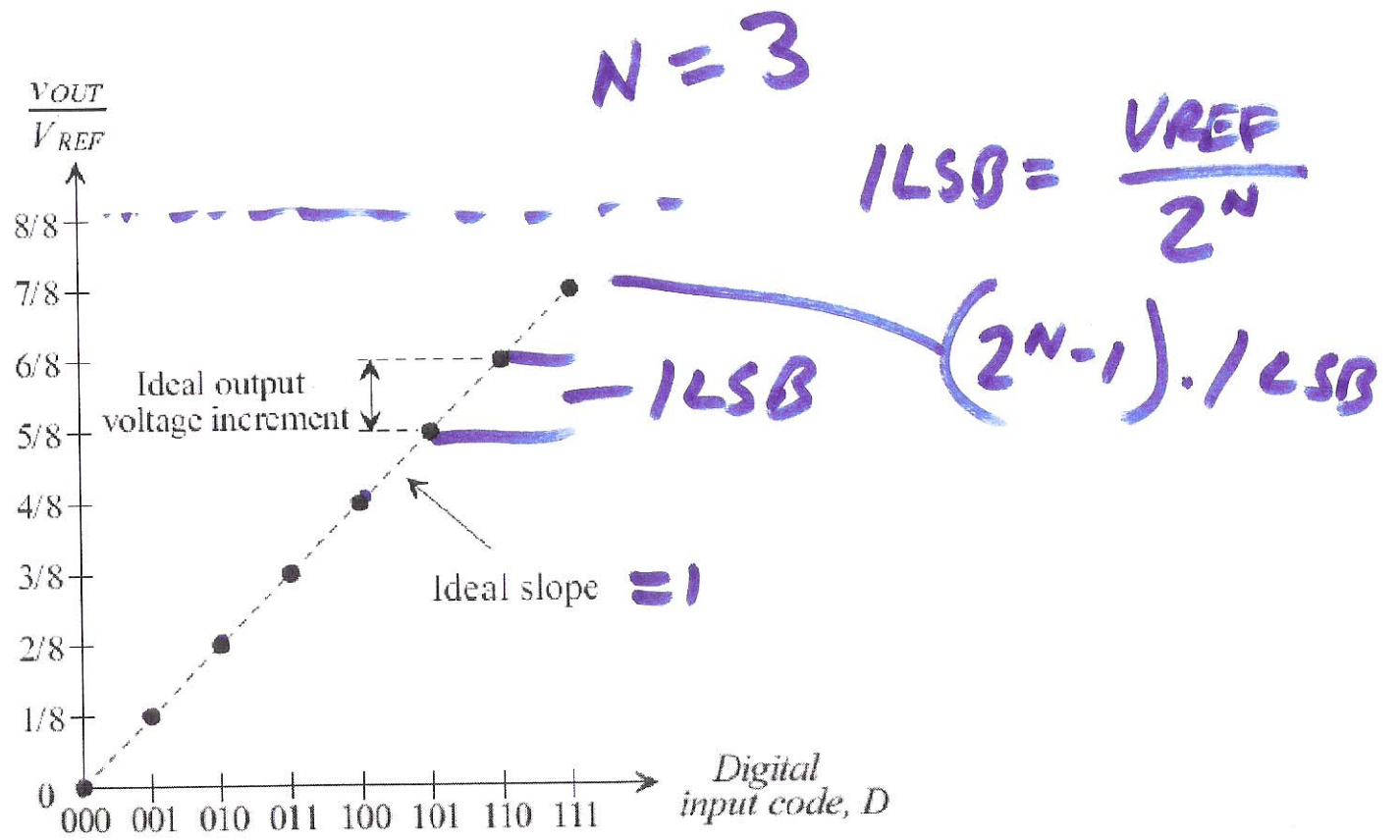
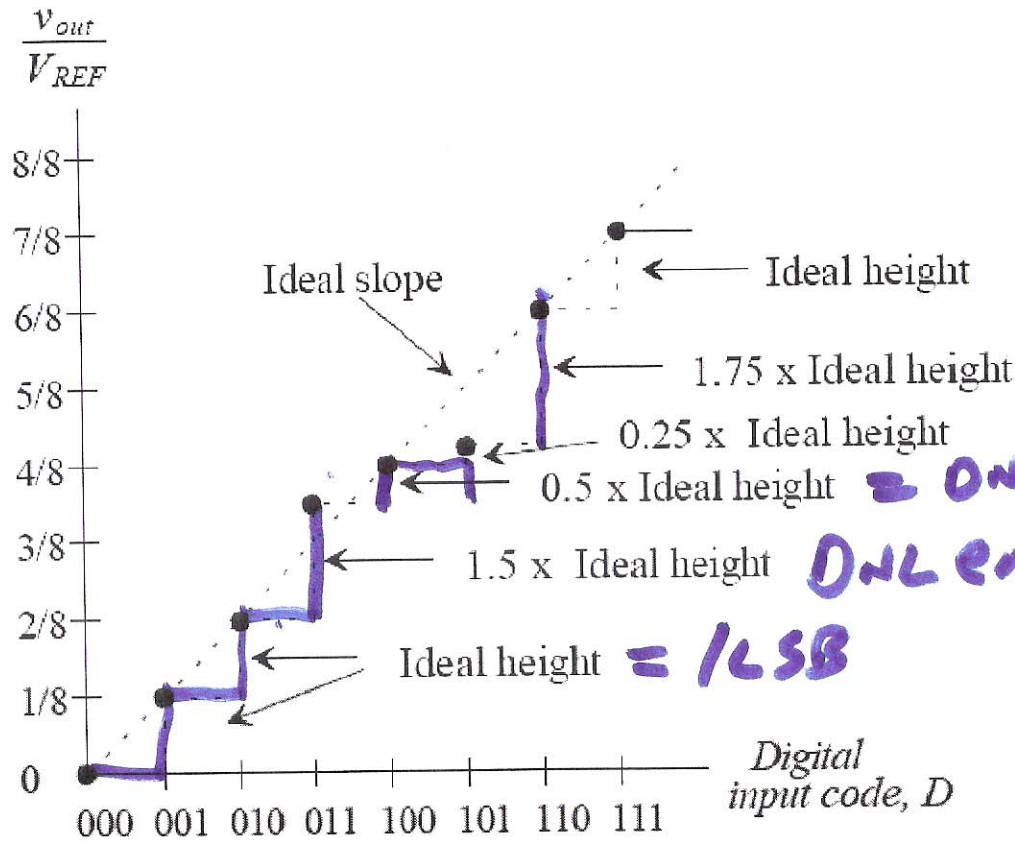
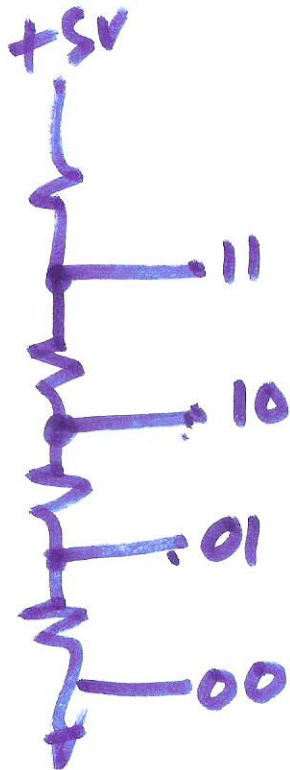


Figure 28.10 Ideal transfer curve for a 3-bit DAC.

10)



$0.25 \times \text{Ideal height} = \text{DNL} = -\frac{1}{2} \text{LSB}$
 $0.5 \times \text{Ideal height} = \text{DNL error } 0.5 \text{LSB}$

Figure 28.11 Example of differential nonlinearity for a 3-bit DAC.

2-bit
Resistor string
DAC

Monotonicity $D \uparrow \rightarrow V_{out} \uparrow$

11)

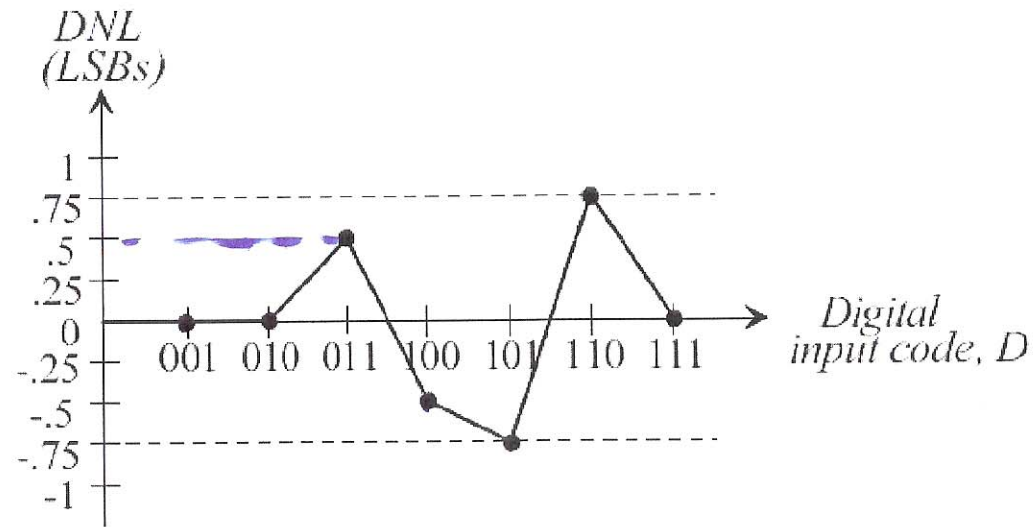


Figure 28.12 DNL curve for the nonideal 3-bit DAC.

12)

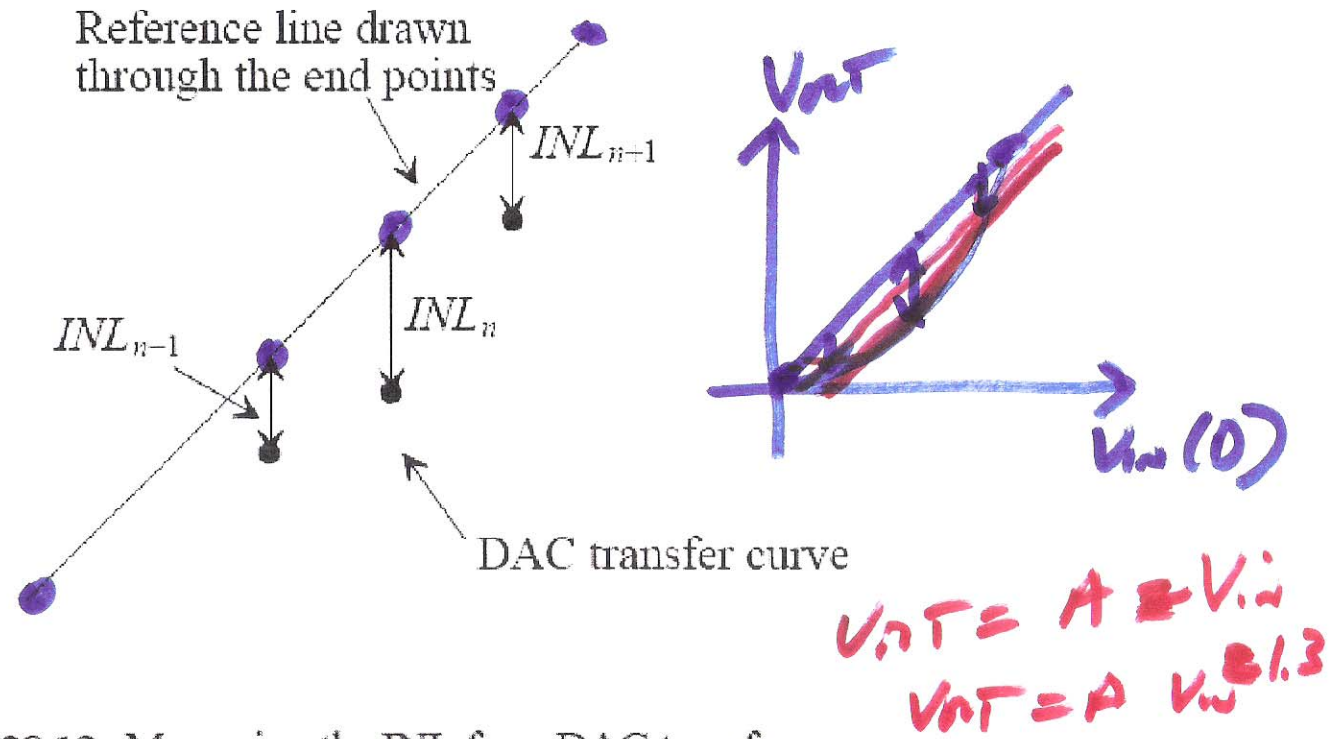


Figure 28.13 Measuring the INL for a DAC transfer curve.

LARGE signal linearity

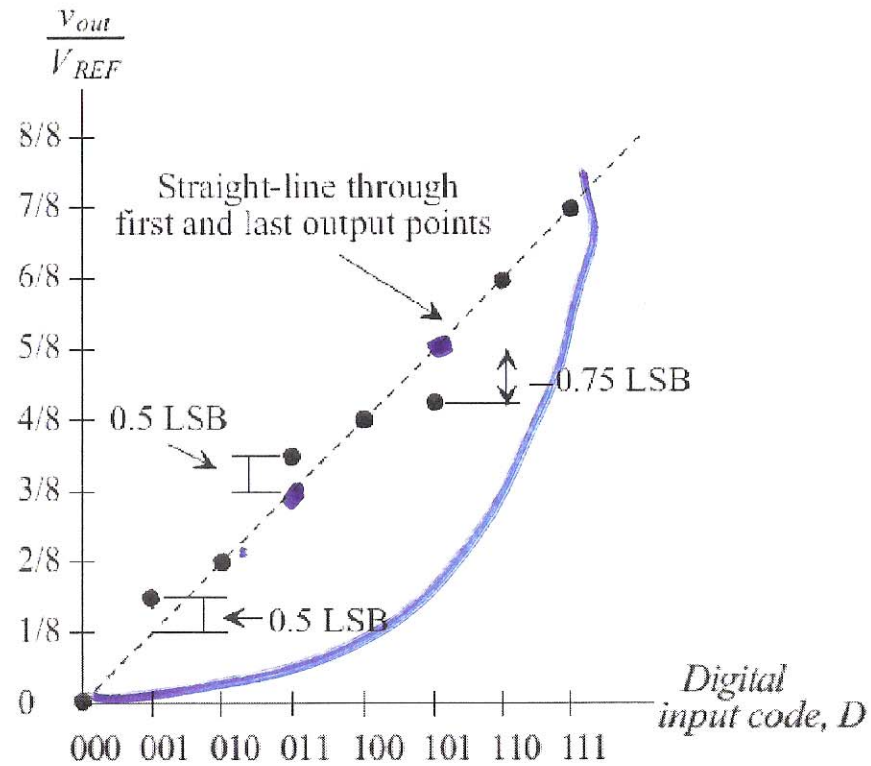


Figure 28.14 Example of integral nonlinearity for a DAC.

14)

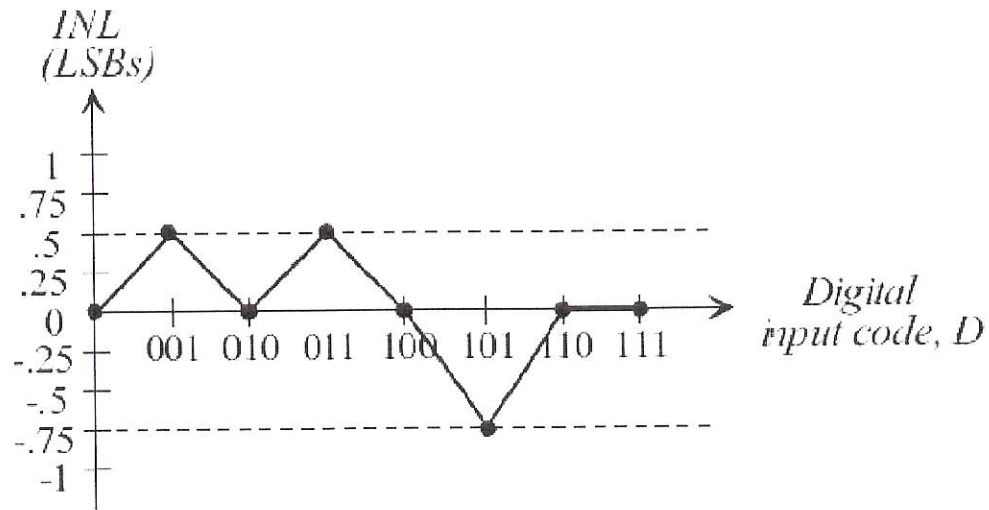


Figure 28.15 INL curve for the nonideal 3-bit DAC.

DNL & INL
are DC measurements

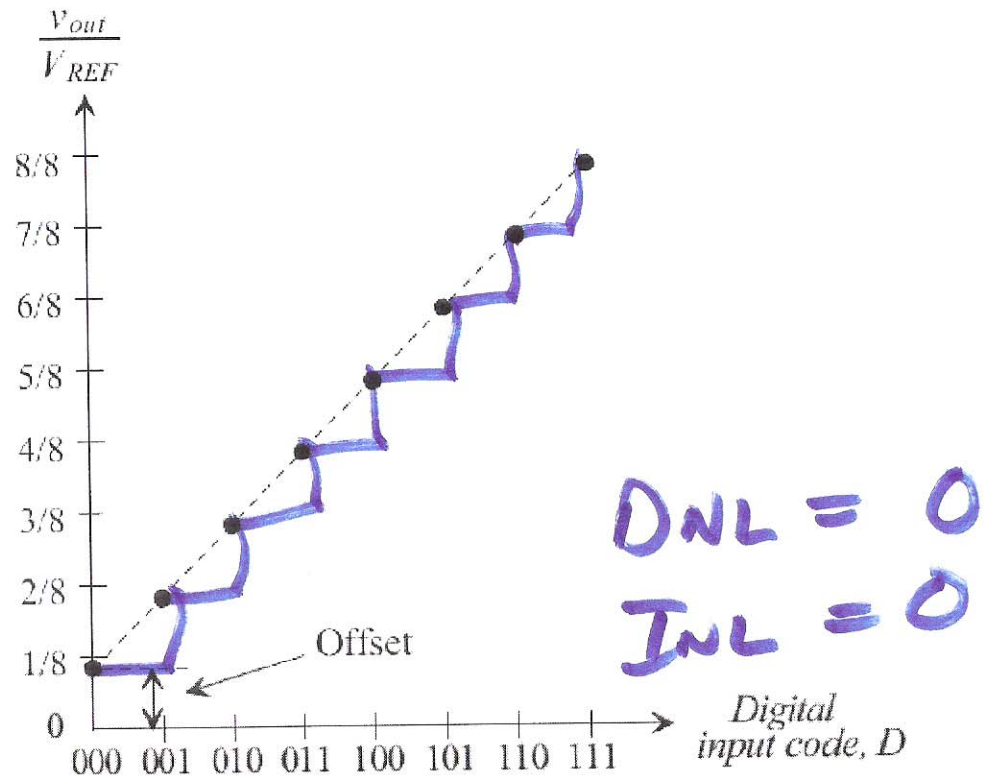


Figure 28.16 Illustration of offset error for a 3-bit DAC.

offset, no distortion

16)

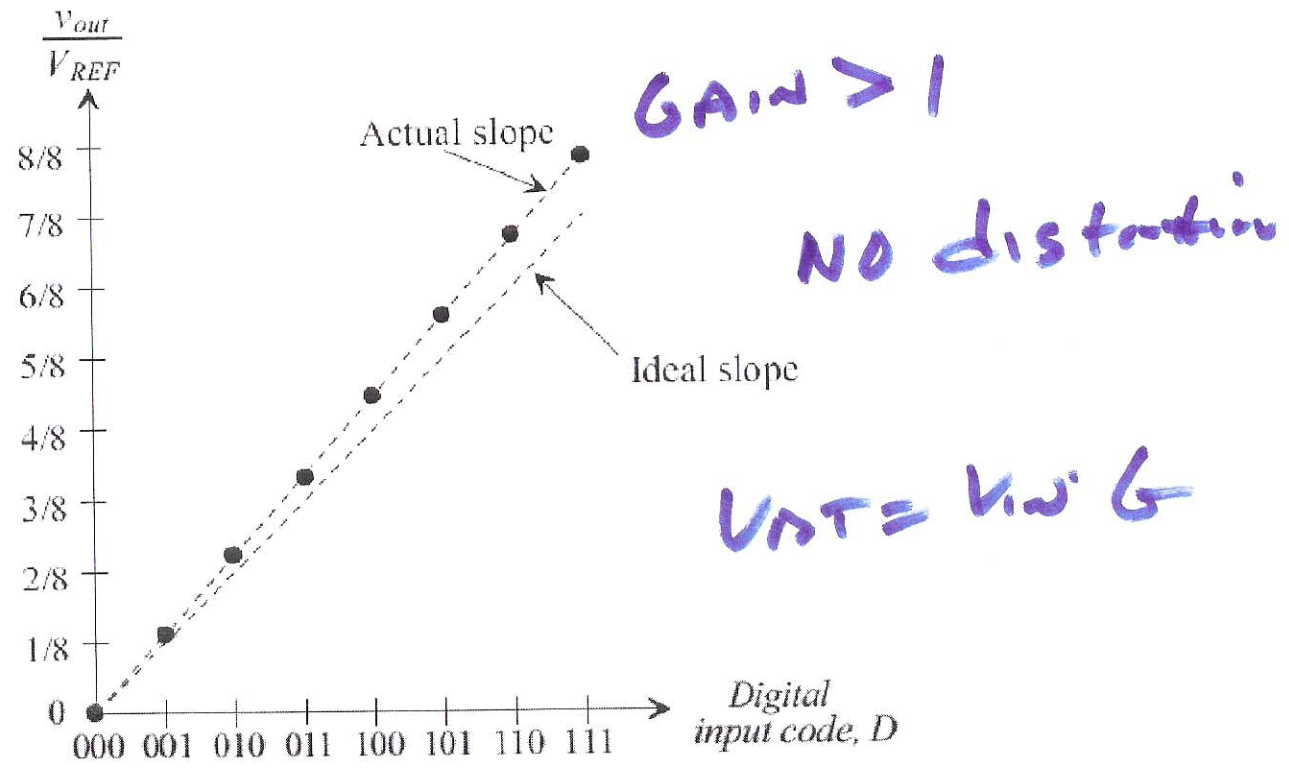


Figure 28.17 Illustration of gain error for a 3-bit DAC.

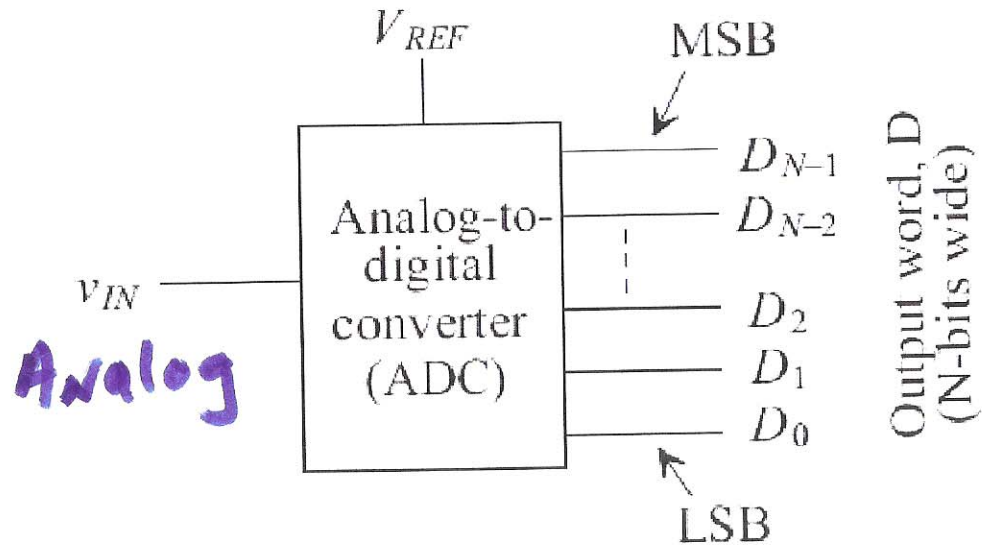


Figure 28.18 Block diagram of the analog-to-digital converter.

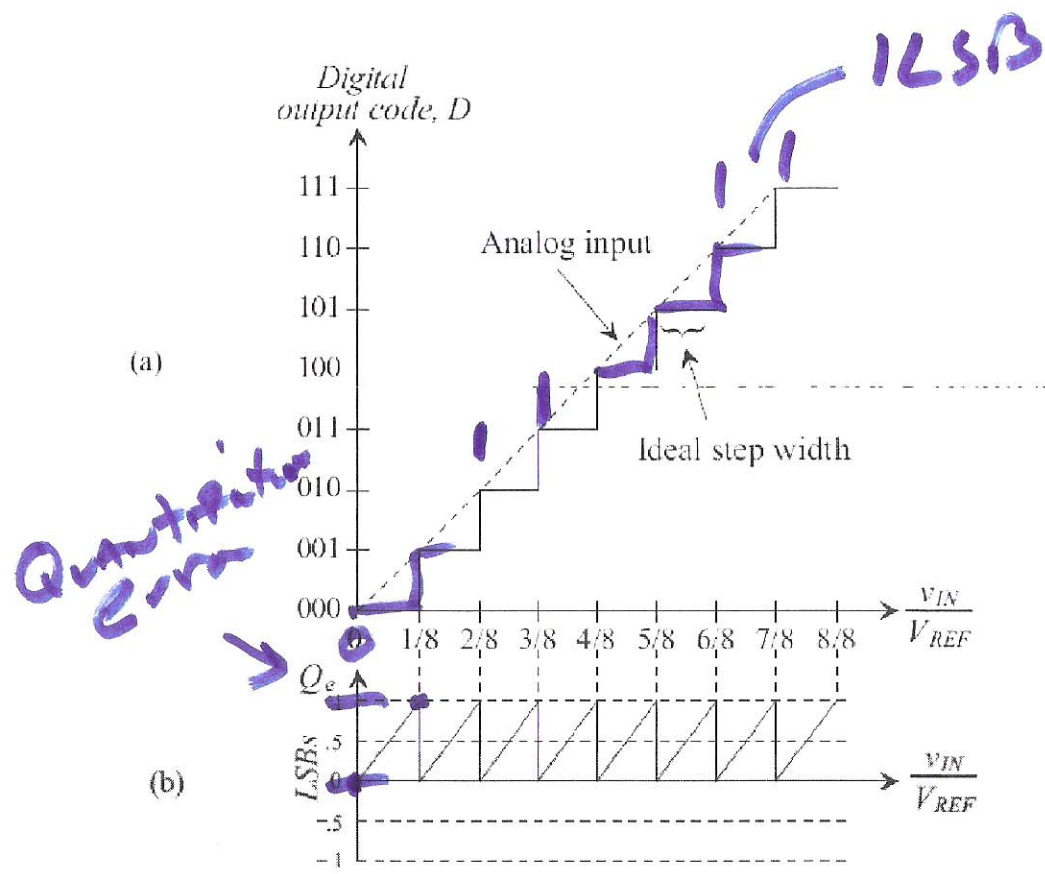


Figure 28.19 (a) Transfer curve for an ideal ADC and (b) its corresponding quantization error.

19)

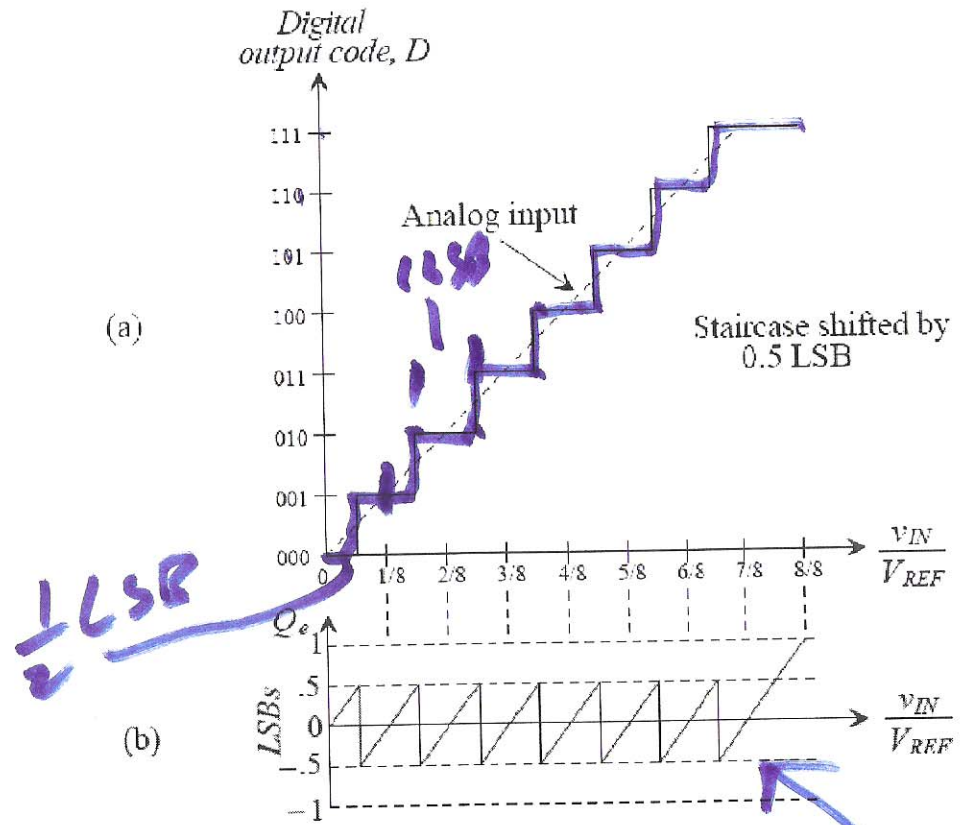
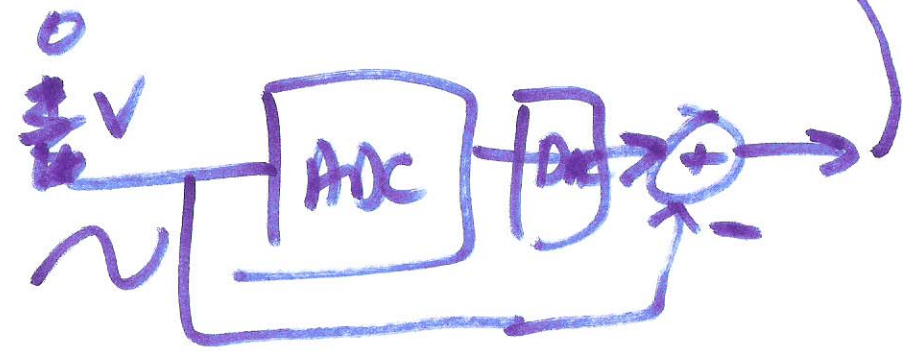


Figure 28.20 (a) Transfer curve for an ideal 3-bit ADC with (b) quantization error centered about zero.

$$V_{rms} = \frac{1 \text{ LSB}}{\sqrt{12}}$$



20)

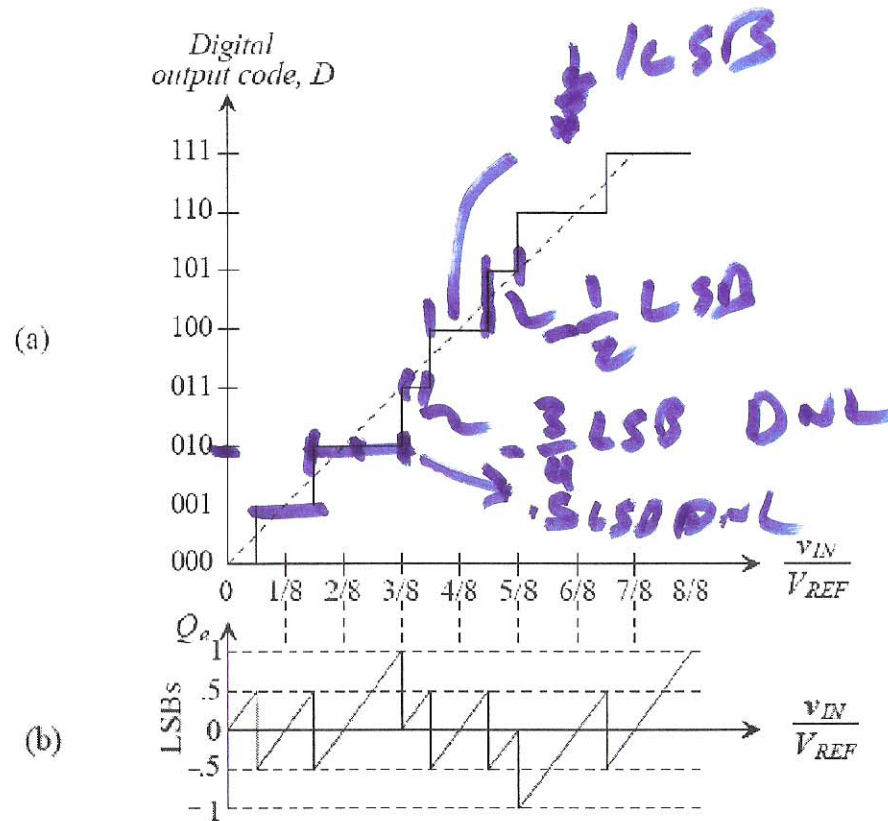


Figure 28.21 (a) Transfer curve for a nonideal 3-bit ADC used in Ex. 28.4 with (b) quantization error illustrating differential nonlinearity.

DNL e R/R_{an}

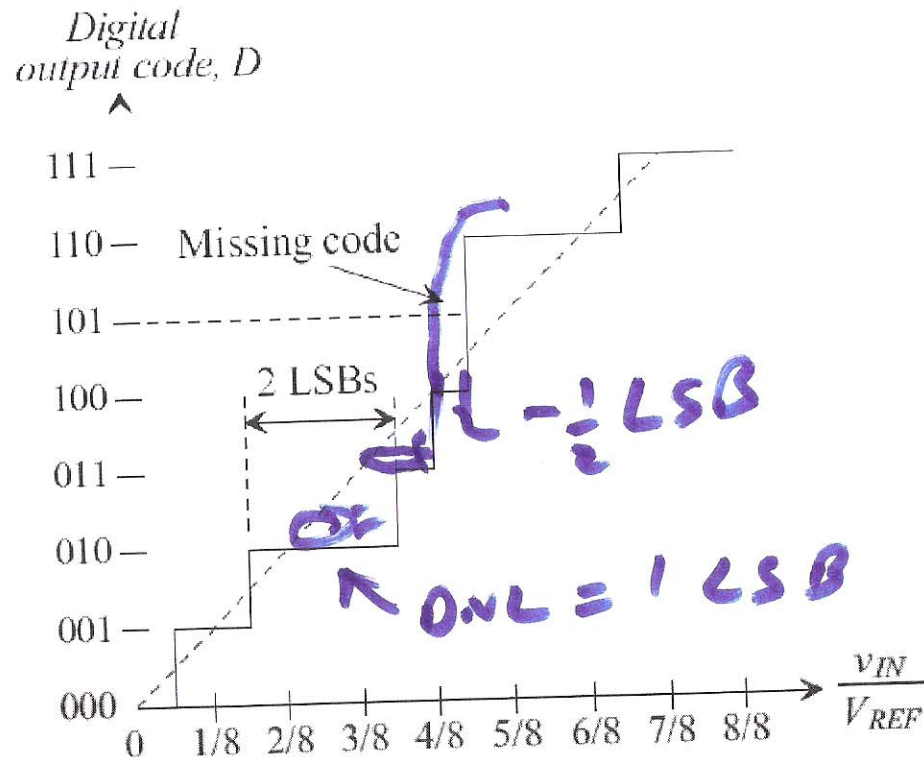


Figure 28.22 Transfer curve for a nonideal 3-bit ADC with a missing code.

22)

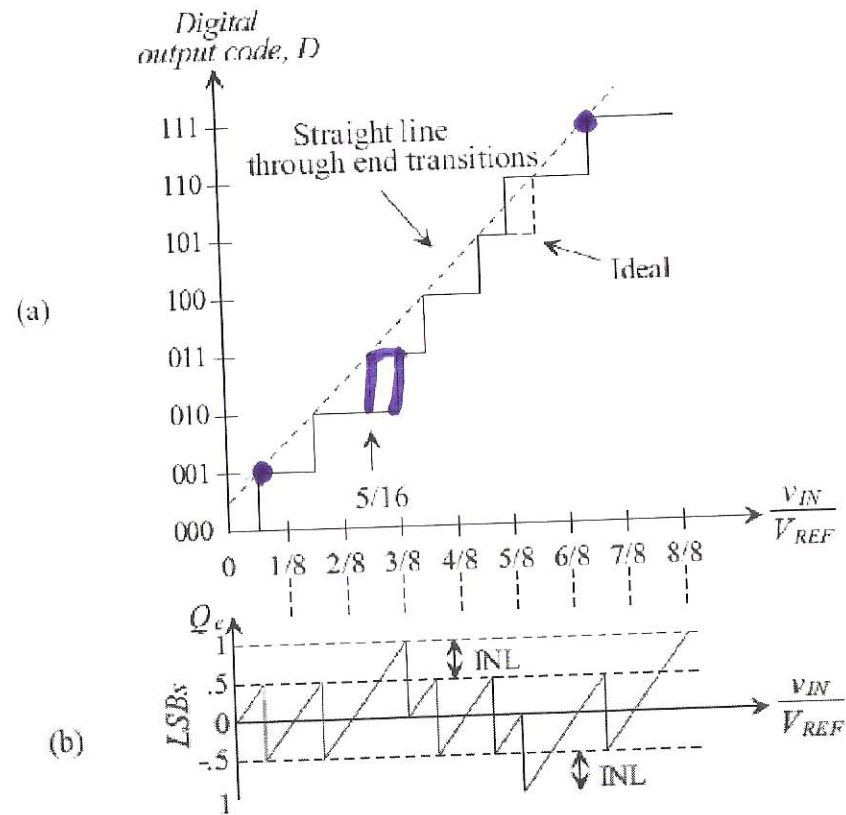


Figure 28.23 (a) Transfer curve of a nonideal 3-bit ADC and (b) its quantization error illustrating INL.

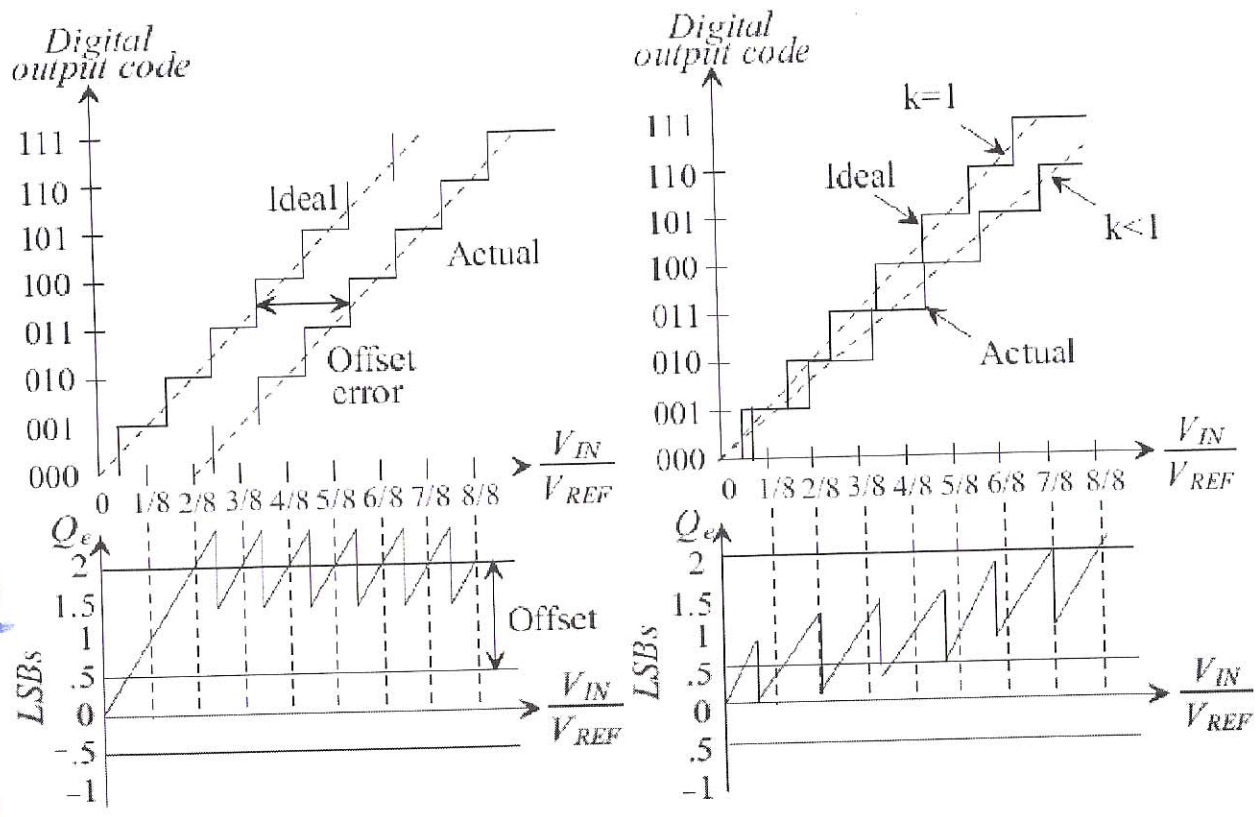
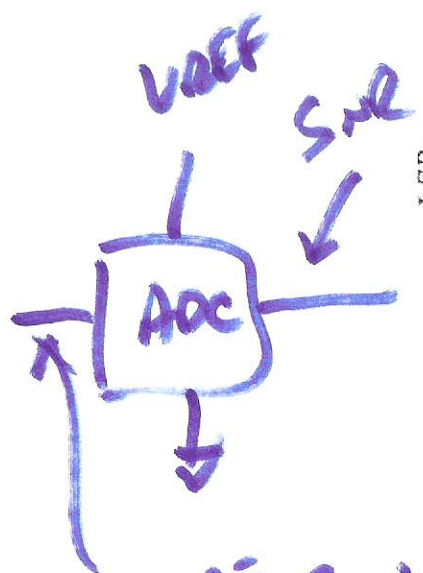


Figure 28.24 Transfer curve illustrating (a) offset error and (b) gain error.



Handwritten notes and formula:

$V_{REF} \sin 2\pi f \cdot t + NO$ distortion SNR

$$SNR_{out} = \frac{\frac{V_{REF}}{2\sqrt{2}}}{\frac{V_{REF}}{2^N}} = \frac{2^N}{2\sqrt{2}} = \frac{2^{N-1}}{\sqrt{2}}$$

24)

Dynamic Range

$$DR = \frac{\text{MAX OUTPUT}}{\text{MIN OUTPUT}} = \frac{V_{REF} - V_{REF}/2^N}{V_{REF}/2^N}$$
$$= 2^N - 1$$

2

$$DR = 20 \log(2^N - 1)$$
$$\approx \underline{\underline{6.02 \cdot N \text{ dB}}}$$

$$\text{SNR} = 20 \log \frac{V_{\text{REF}}/2\sqrt{2}}{Q_e}$$

$$= 20 \log \frac{V_{\text{REF}}/2\sqrt{2}}{V_{\text{LSB}}/\sqrt{12}}$$

$$= 20 \log \frac{V_{\text{REF}}/2\sqrt{2}}{V_{\text{REF}}/(2^N \cdot \sqrt{12})}$$

$$= \frac{2^N \cdot \sqrt{12}}{2\sqrt{2}} = 2^{N-1} \cdot \sqrt{6}$$

$$\text{SNR} = 20 \log (2^{N-1} \cdot \sqrt{6})$$

$$= 20 \log 2 \cdot (N-1) + 20 \log \sqrt{6}$$

$$= \overset{6.02}{\cancel{7.78}} (N-1)$$

$\text{SNR} = 6.02N + 1.76$

ENOB

26)

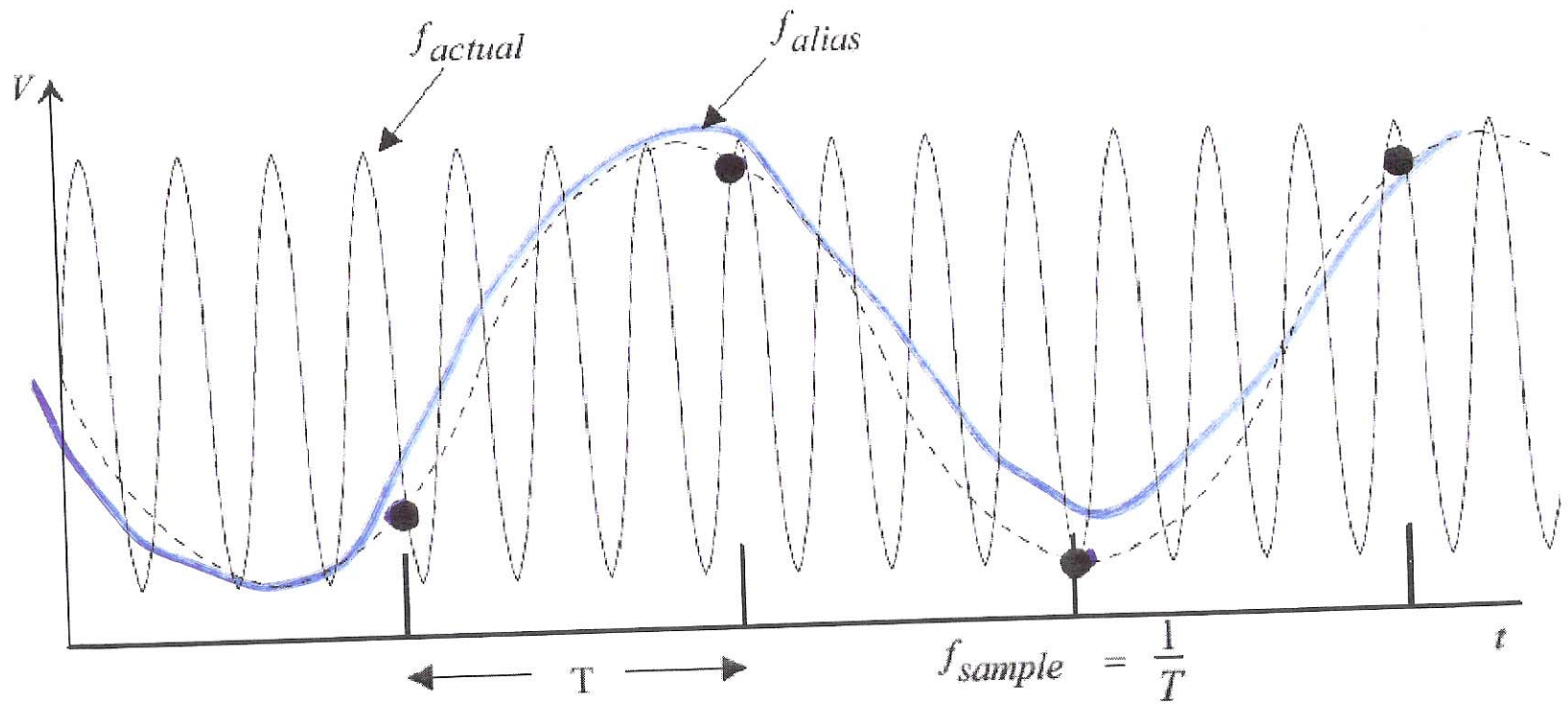


Figure 28.25 Aliasing caused by undersampling.

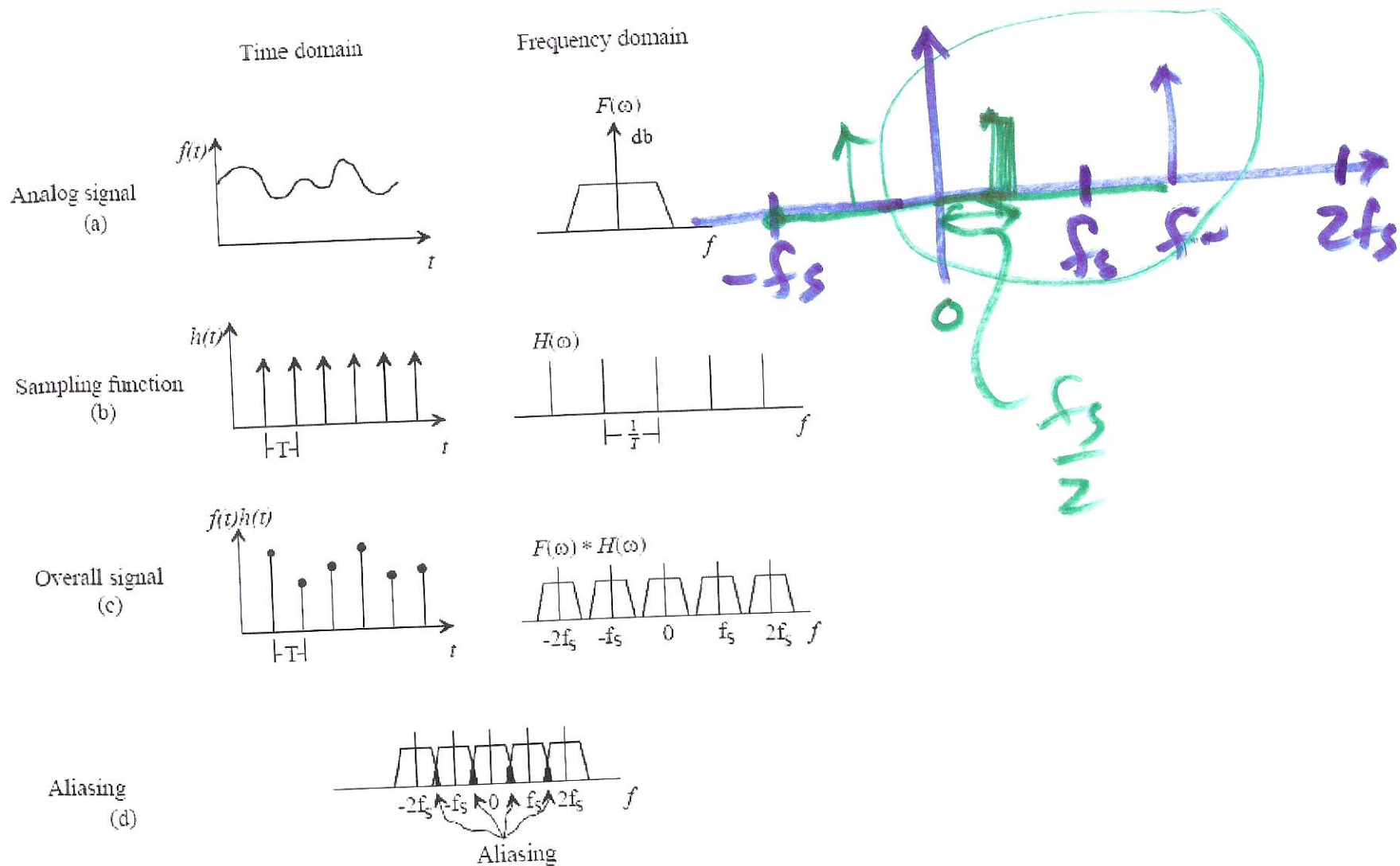


Figure 28.26 Illustration of aliasing in the time and frequency domain. (a) The analog signal; (b) the sampling function; (c) the overall signal; and (d) aliasing in the frequency domain.