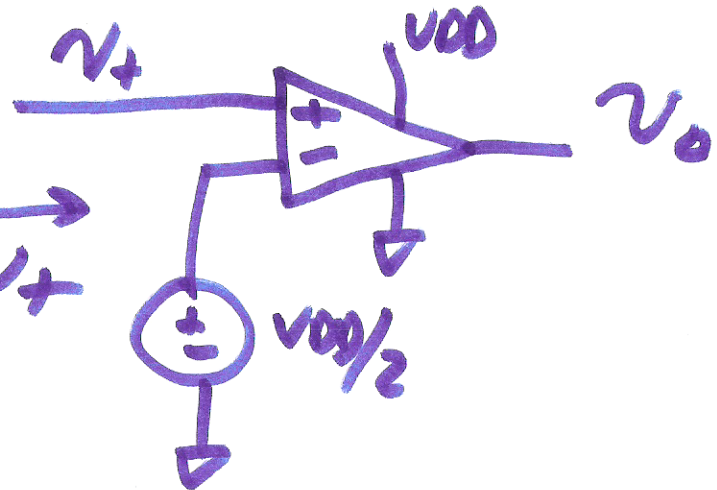
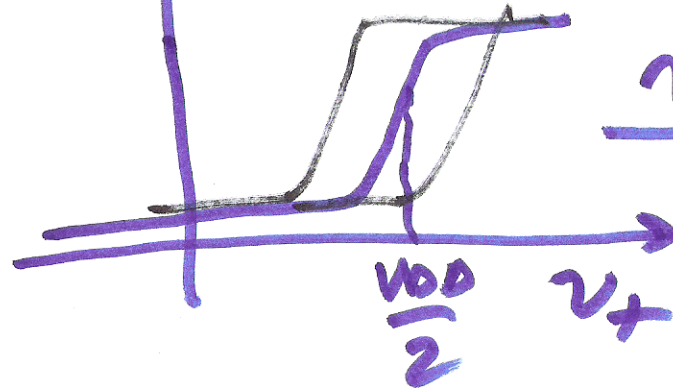
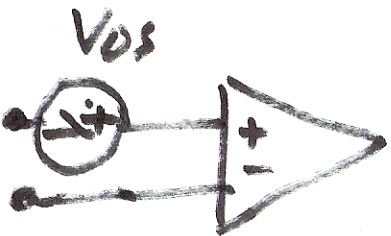
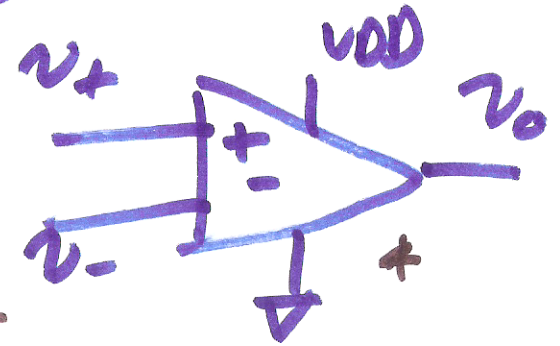
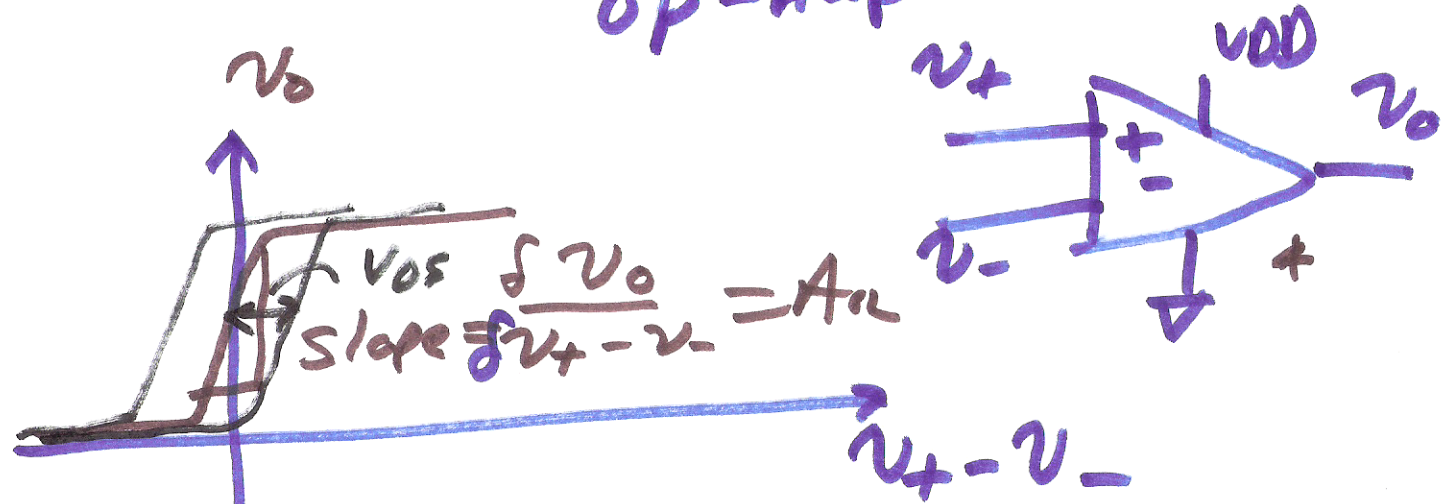
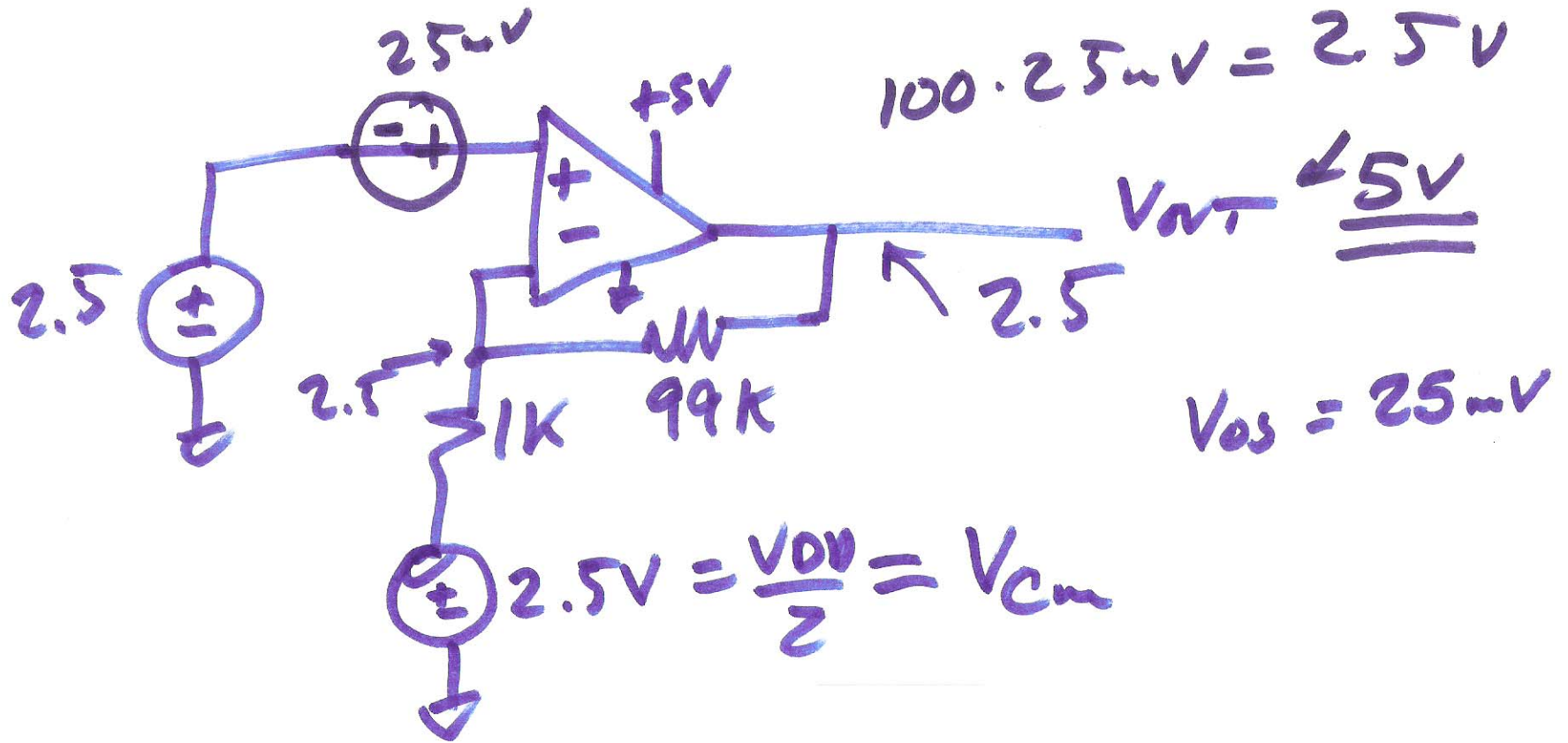


# 25.4 Circuits

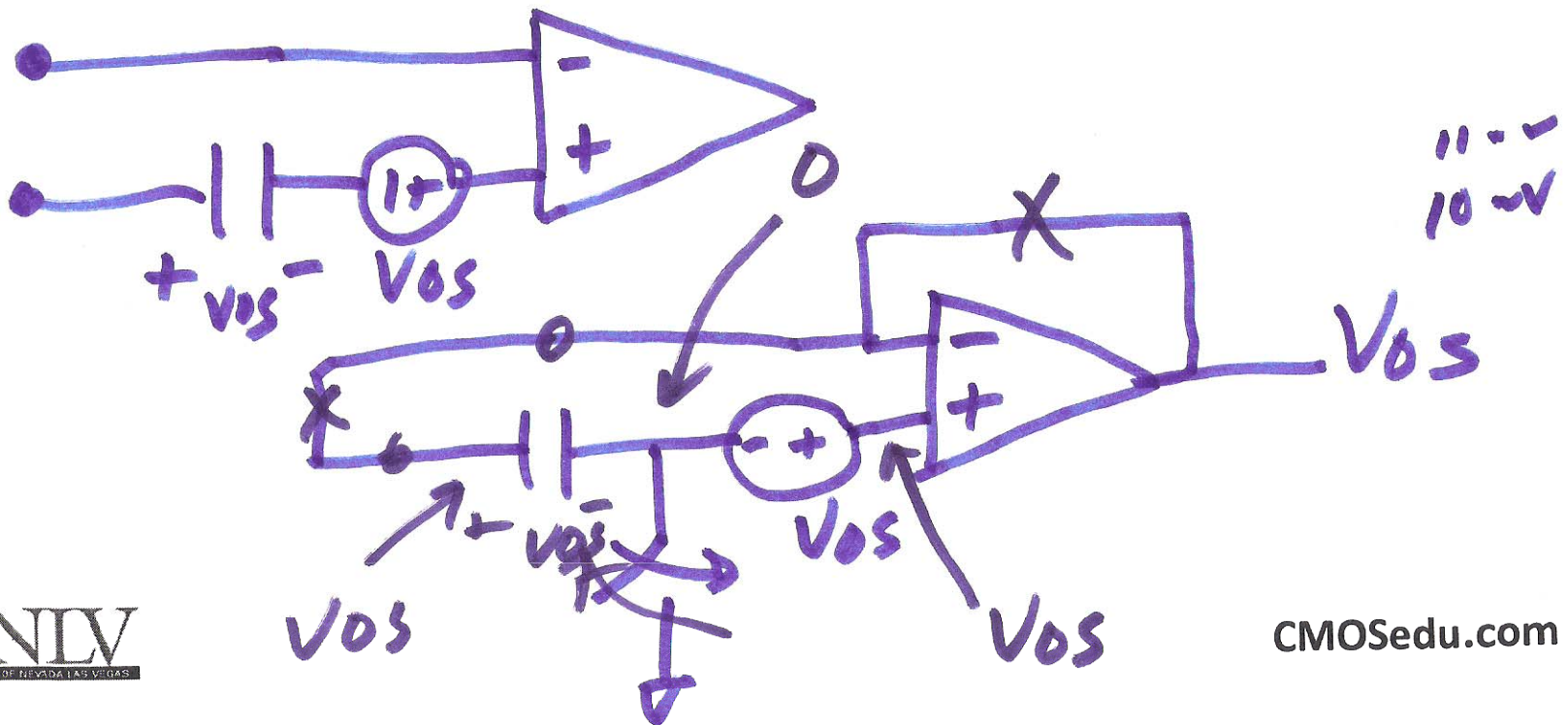
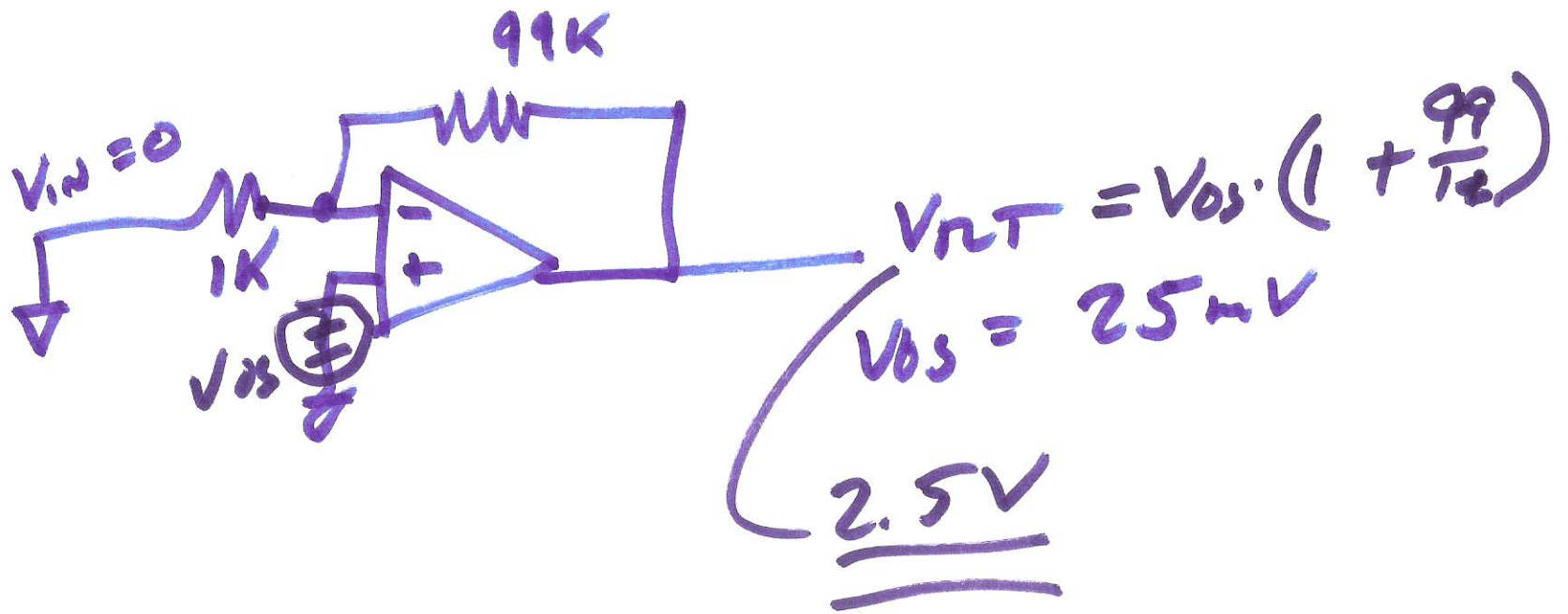
Reducing effects of  
op-amp offset

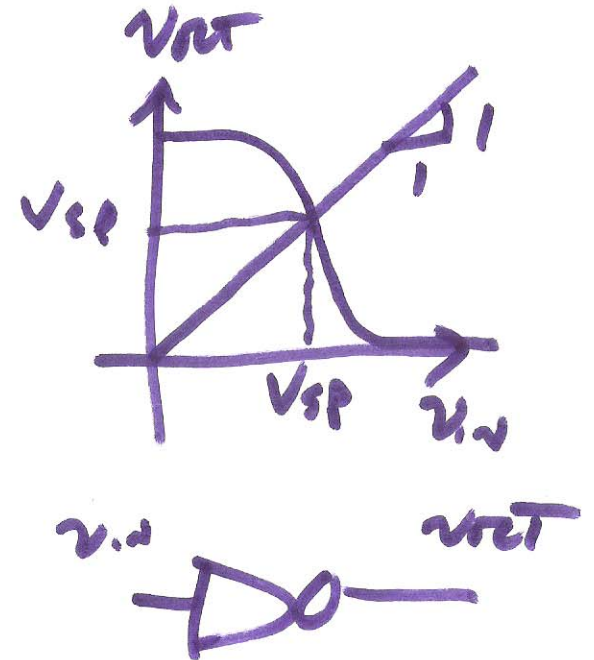
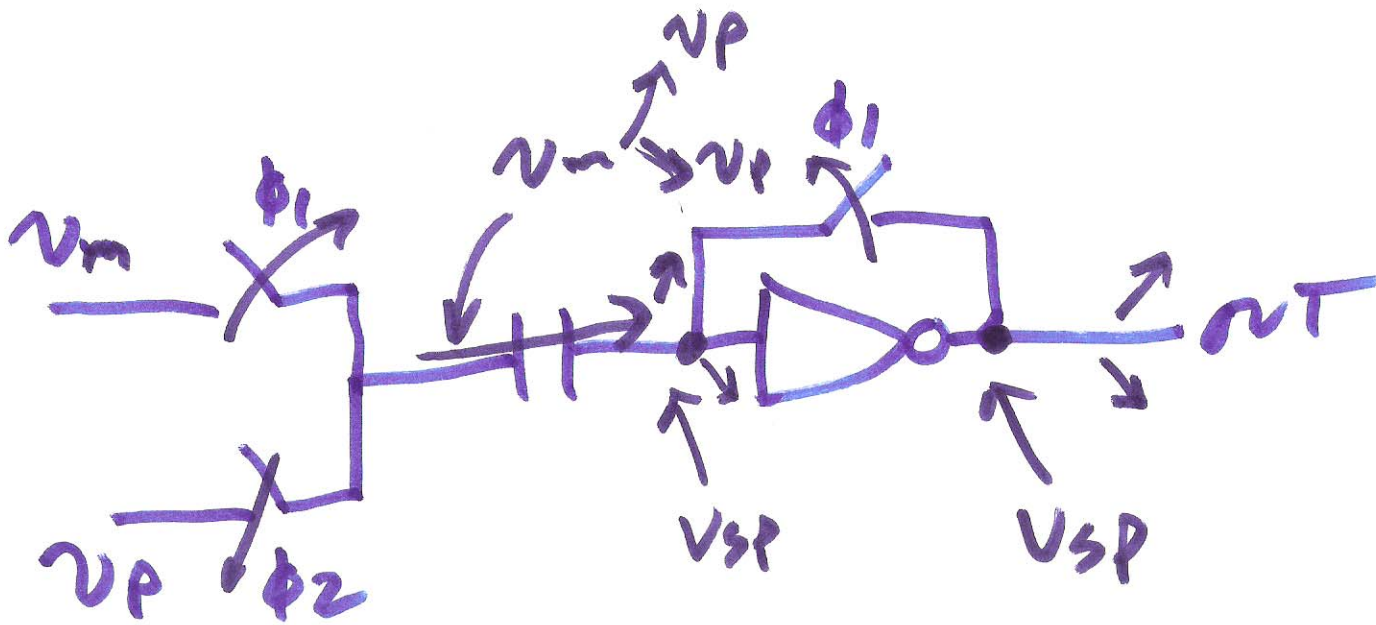


Give an example where offset is an issue



2)



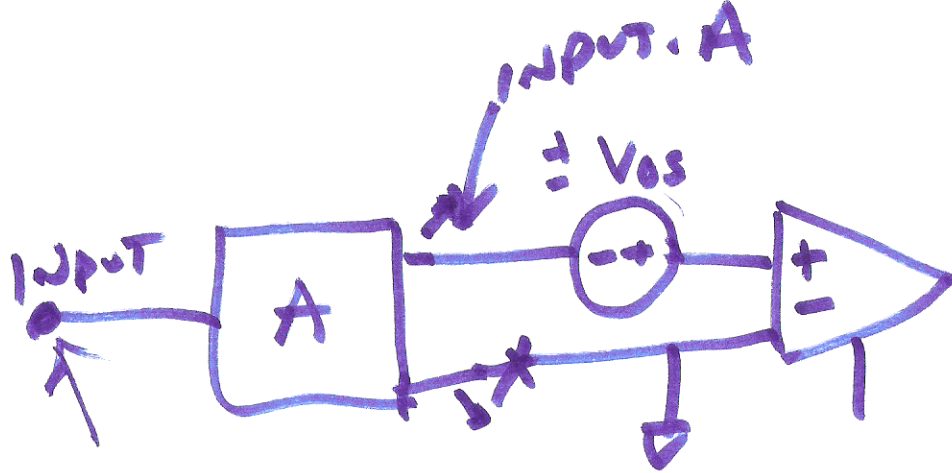


$$v_m - v_{sp}$$

$$v_p - v_m > 0 \quad 0$$

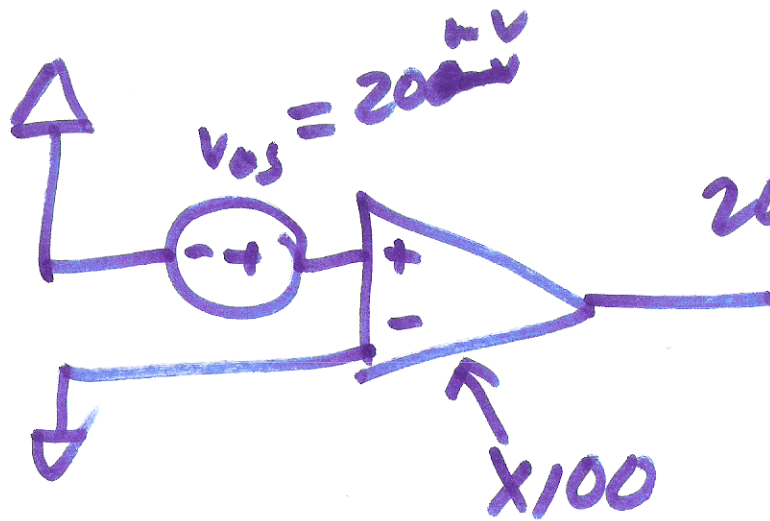
$$v_p - v_m < 0 \quad 1$$

4)



$$\text{INPUT} \cdot A = V_{OS}$$

$$\text{INPUT} = \frac{V_{OS}}{A}$$



$$200 \mu\text{V} \cdot 100 = 2\text{V}$$

5)