Lecture 7
ECG 721
Sept. 16, 2015
Flash memory

Review basic MOS

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Control GATE

Control gate

WL

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Erased condition means small $V_{TH}$

LUT $\rightarrow$ Physical

Logical $\rightarrow$ 1

$4$ wells neg.

$3$
Channel Hot Electron Injection (programming)

NOR

$NOR$ is less dense than NAND!
Programming FB NAND memory

float +10V → 11V - 12V - 13V - 14V

program

verify.

N-well
P-well

11V 12.3V +7V

+10V

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