

# Shada Sharif

sharifshada@gmail.com

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Goal: To attain a career as an analog/mixed signal design engineer.

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## EDUCATION

**University of Nevada, Las Vegas**, Howard R. Hughes College of Engineering

B.S., Electrical Engineering; Magna Cum Laude; Dec 2016; GPA: 3.95

*Coursework:*

- ❖ Solid State Device Fundamentals; Microelectronics; Electromagnetics; Digital Logic Design; Signals and Systems; Feedback and Control Systems; Programming with C++; Professional Ethics
- ❖ Labs: Electronics; Control System Simulation; Circuit Design; Microcontroller Systems Design

**University of Nevada, Las Vegas**, Howard R. Hughes College of Engineering

M.S., Electrical Engineering; Dec 2018; GPA: 4.00

*Concentrations:*

- ❖ Electronics, Communication, Power Systems, and Solid State

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## LEADERSHIP

**IEEE: Member** | Nov 2013 – present

- ❖ Attended general meetings, and guest speakers events

**Practical Electronics Club Vice President** | Nov 2013 – present

- ❖ Organized club meetings, and suggested future projects

**Tau Beta Pi, Engineering Honor Society Member** | May 2015 – present

- ❖ Officially initiated on May 2<sup>nd</sup>, 2015

**AEE: Member** | Oct 2017 – present

- ❖ Attending general meetings

**Society of Women Engineers: Member** | Jan 2018 – present

- ❖ Attending future general meetings

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## EXPERIENCE

**INDEPENDENT STUDY (Delta Sigma Modulator ADC using 130nm fabrication process)**

- ❖ Designing DSM ADC, fabrication on a chip, and testing chip
- ❖ Designing KD1S ADC, DSM that involves time interleaving

**MAX TECH & BEYOND**

- ❖ Field tests of a power optimizer on home appliances to help in energy savings

**LAB ASSISTANCE**

- ❖ Soldered components on circuit boards; surface mount
- ❖ Assembled chassis kits for beginner engineering classes

**TEACHING ASSISTANCE**

- ❖ Assisting graduate students and grading class homework

**INDEPENDENT STUDY (Fine Grained Classification/Machine Learning)**

- ❖ Experience with installing Caffe on Ubuntu
- ❖ Creating a Dataset for training a machine using FGC

**SENIOR DESIGN**

- ❖ Orthopedic boot add-on device for measuring pressure, displays pressure, count steps, and stores data
- ❖ Used to prevent compartment syndrome disease

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## SKILLS

- ❖ Software: C++, Python, LTSpice, Microsoft Office, MATLAB, Altium PCB, KiCAD PCB, Atmel Studio, Quartus, Cadence Spectre & Virtuoso, Caffe
- ❖ Hardware: oscilloscope, function generator, power supply, digital multimeter
- ❖ Technical: soldering, troubleshooting, wire bonding

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## AWARDS

- ❖ 2<sup>nd</sup> place winner in Electrical & Computer Engineering at Fred & Harriet COX Senior Design Competition