

Stacking Power MOSFETs

NMOS Configuration

By Kendrick De La Pena

Updated: 04/18/2014

Test 1

- NMOS Configuration; 5 Stack MOSFET
- MOSFET:
 - STP8NM60 , $V_{DS} = 600 \text{ V}$, $R_{DS(on)} = 0.9 \text{ Ohms}$
- Calculated Capacitance Values:
 - 160pF, 320pF, 480pF, 640pF
- Diode:
 - BZX84C15L
- Max Voltage:
 - 2500 V

Test 1 – Calculations

$$C_{gs} = 700 \text{ pF}$$

$$C_{gd} = 100 \text{ pF}$$

$$C_{zener} = 110 \text{ pF}$$

$$V_d = 500 \text{ V}$$

$$V_{gs} = 20 \text{ V}$$

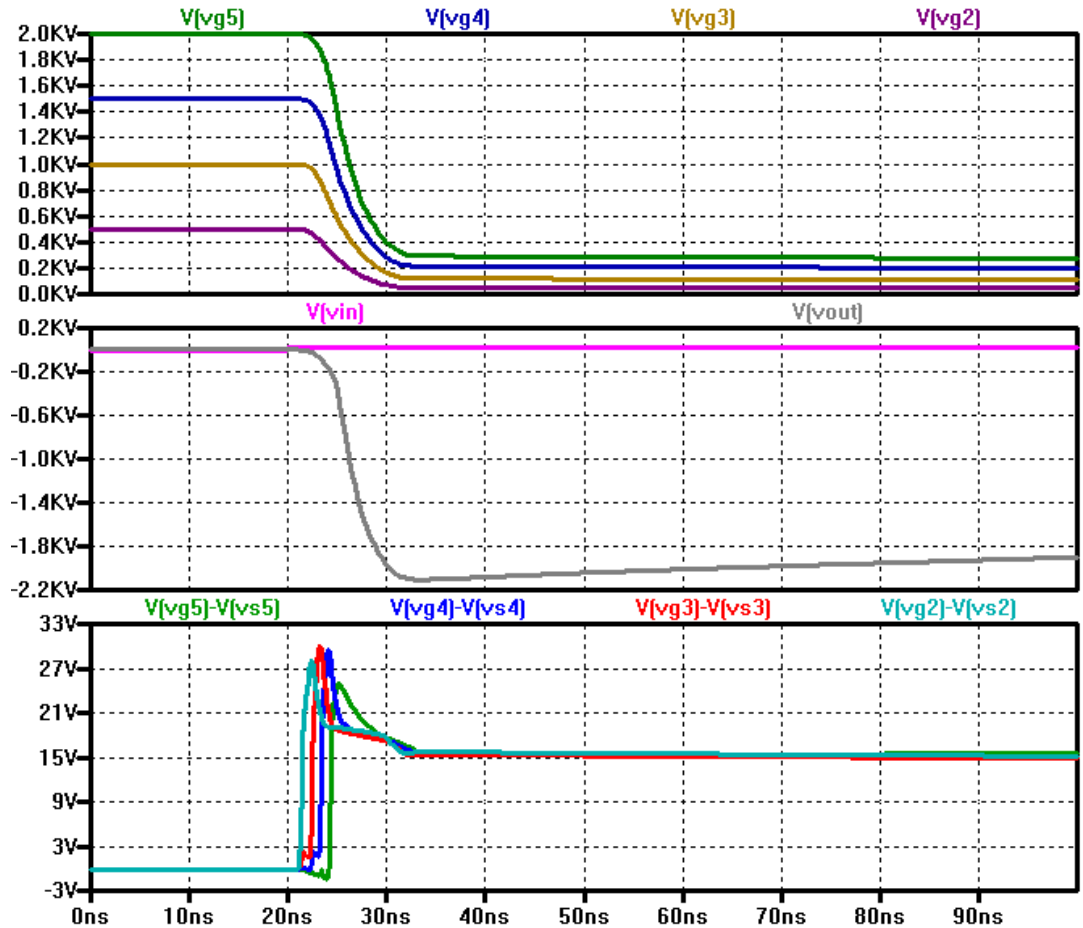
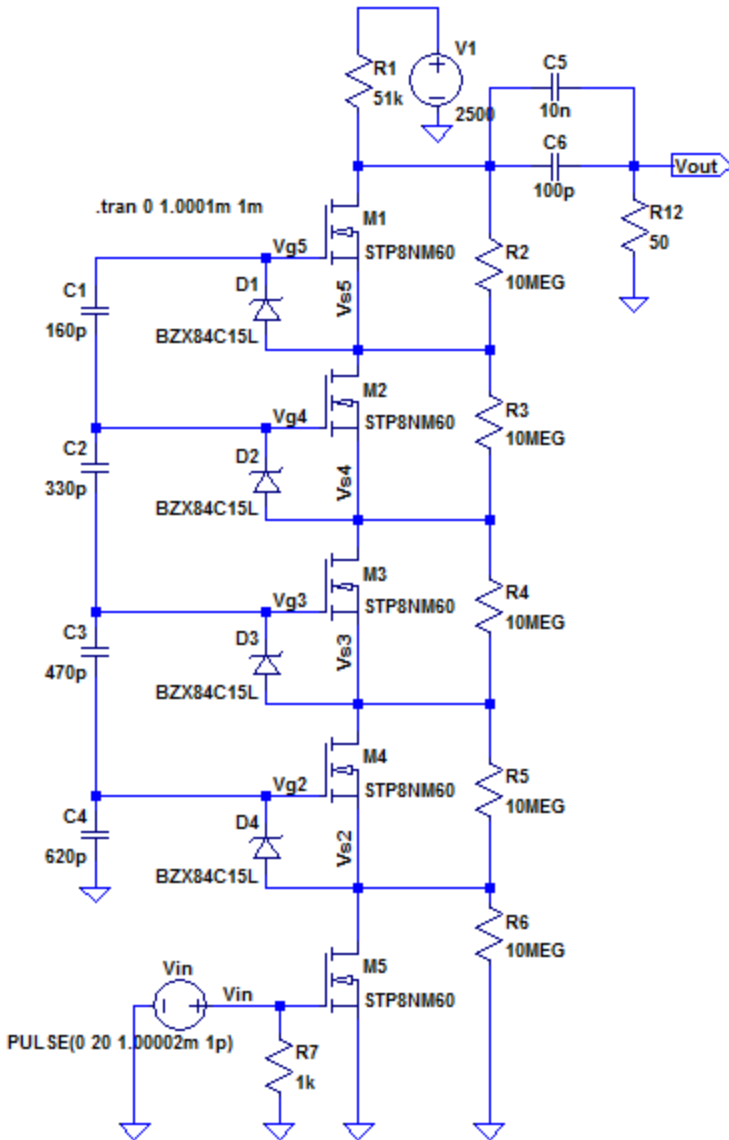
$$A_v = 25$$

$$\begin{aligned} C'_{gs} &= C_{gs} + A_v * C_{gd} + C_{zener} \\ &= 700 \text{ pF} + 25 * 100 \text{ pF} + 110 \text{ pF} \\ &= 3310 \text{ pF} \end{aligned}$$

$$\begin{aligned} V_{gs} &= V_d * C_2 / (C_2 + C'_{gs}) \quad \text{Solve for } C_2 \\ C_2 &= [(V_{gs} / V_d) * C'_{gs}] / [1 - (V_{gs} / V_d)] \\ &= [(20 / 500) * 3310\text{p}] / [1 - (20 / 500)] \\ &= 138 \text{ pF} \end{aligned}$$

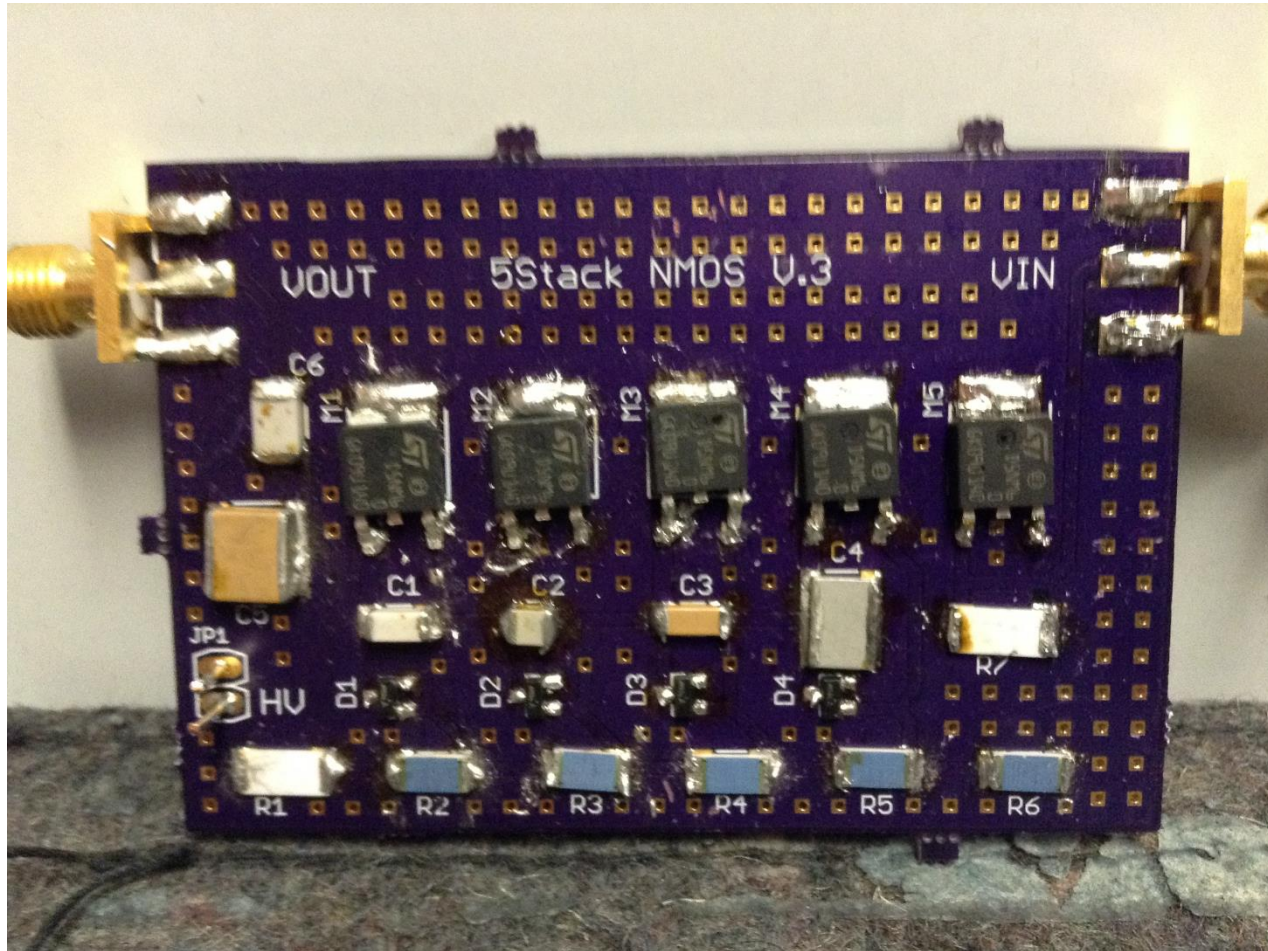
*To ensure the MOSFETs turn on, increase C2 to **160 pF***

Test 1 – Simulation & Values

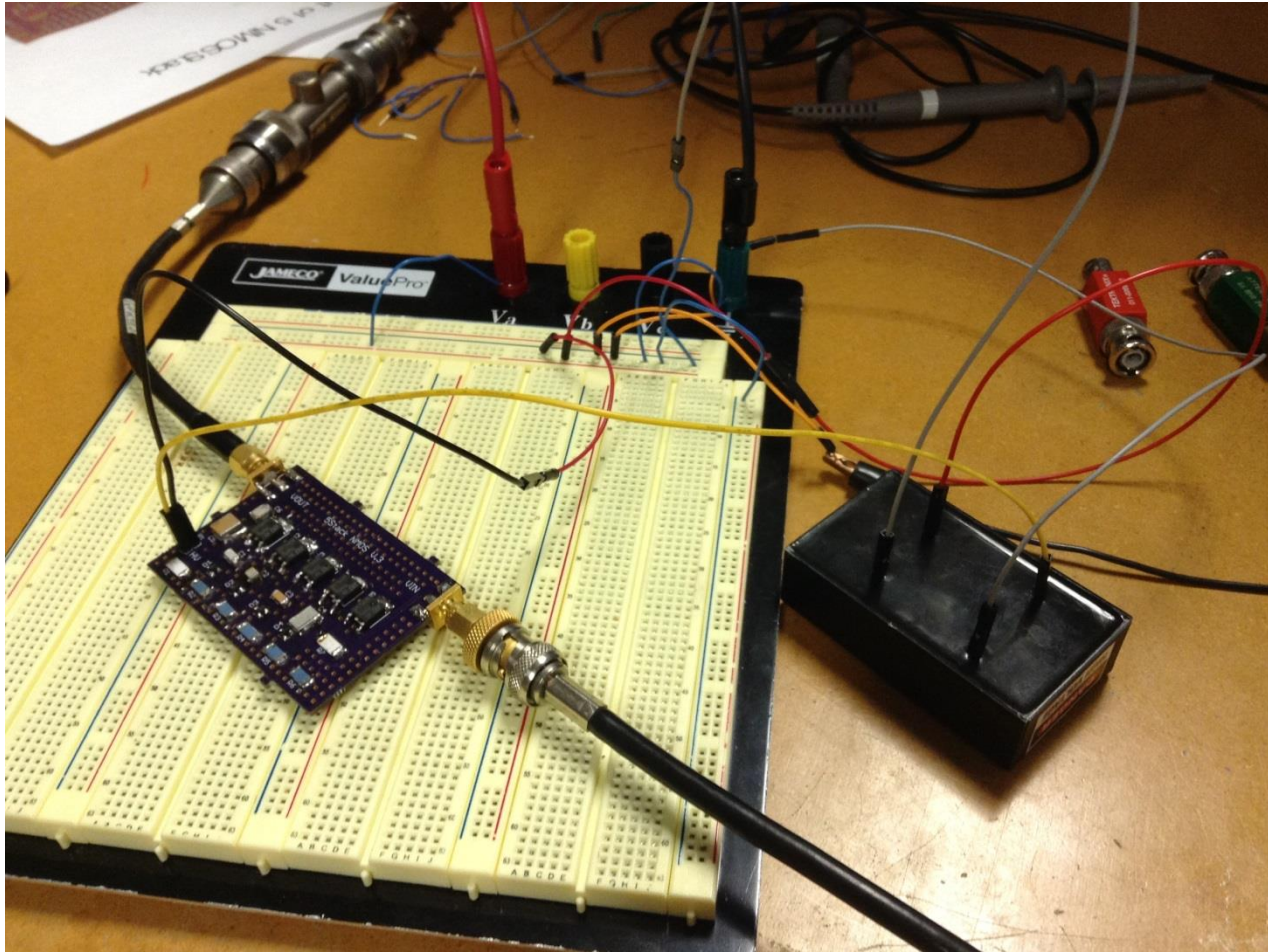


*Values reflect components available

Test 1 – Chip

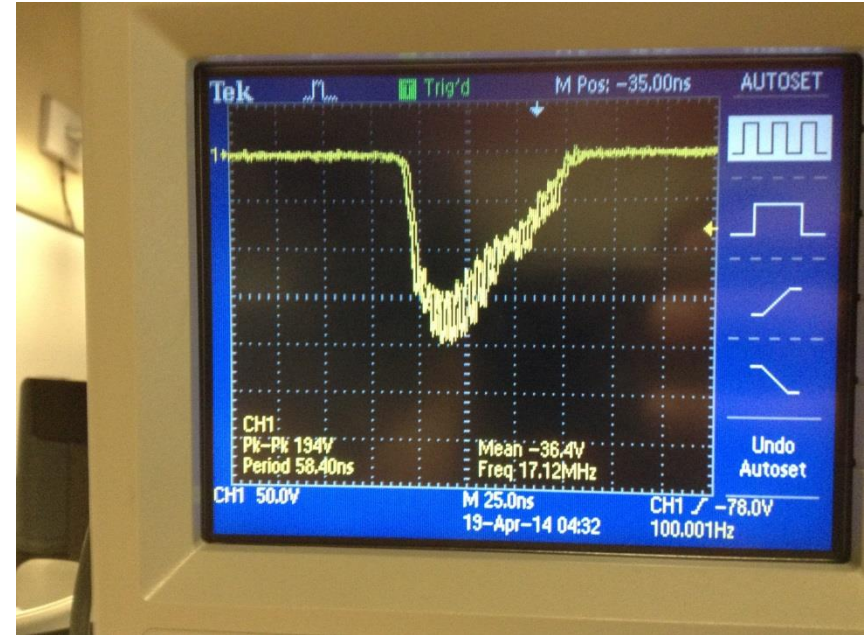


Test 1 – Setup



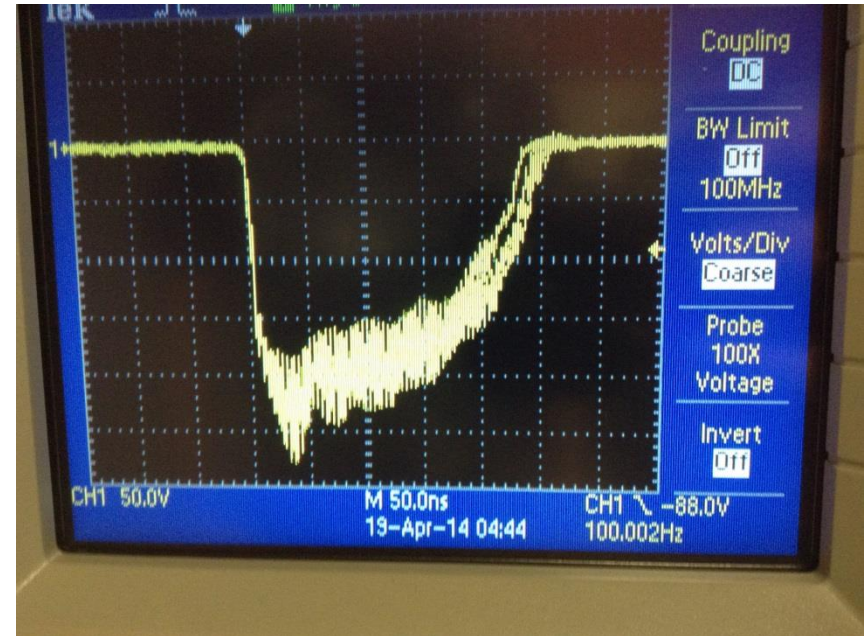
Test 1 – Sample 1 Results

- $V_{in} = 525\text{ V}$
- Switching = 200 V, or 38.1%
- Voltages Across (Difference):
 - M1: 520 V (-5)
 - M2: 246 V (-274)
 - M3: 177 V (-69)
 - M4: 76 V (-101)
 - M5: 19 V (-57)
- The waveform oscillates both when switching from and returning to zero volts
- The noise may be caused by the Zener diodes as they are not reacting fast when the voltage switches



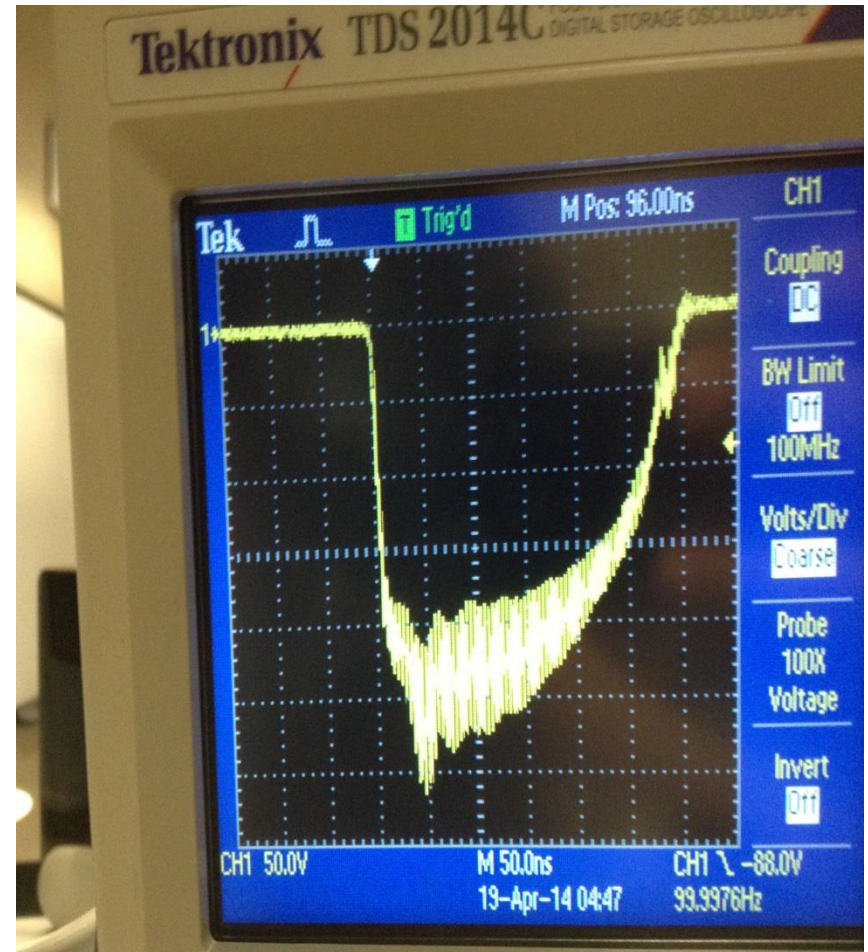
Test 1 – Sample 2 Results

- $V_{in} = 747\text{ V}$
- Switching = 275 V, or 36.8%
- Voltage Across (Difference):
 - M1: 730 V (-17)
 - M2: 440 V (-290)
 - M3: 303 V (-137)
 - M4: 156 V (-147)
 - M5: 25 V (-131)
- Similar results to the previous sample



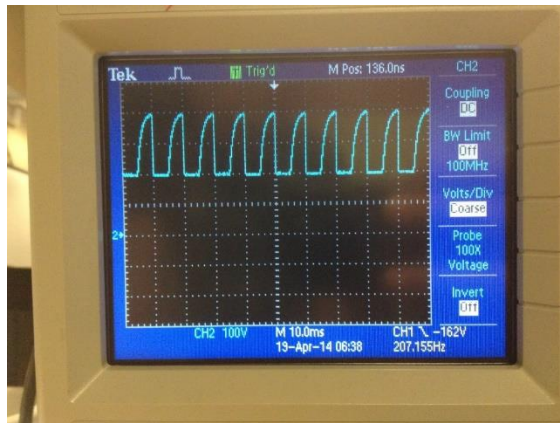
Test 1 – Sample 3 Results

- $V_{in} = 1.008 \text{ kV}$
- Switching = 320 V, or 31.7%
- Voltage Across (Difference):
 - M1: 990 V (-18)
 - M2: 579 V (-411)
 - M3: 398 V (-181)
 - M4: 258 V (-140)
 - M5: 39 V (-219)
- Results were consistent, but the percentage of switching progressively got worse

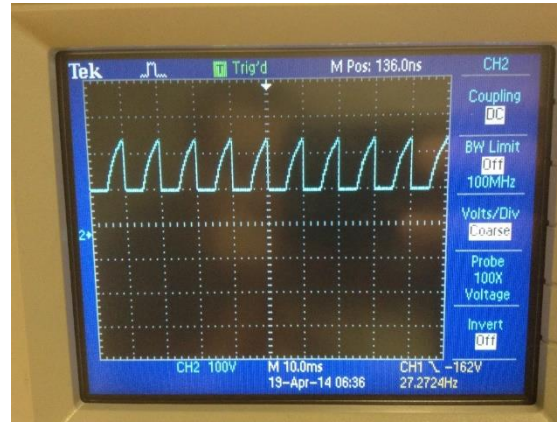


Test 1 – Sample 1 Gate Probing

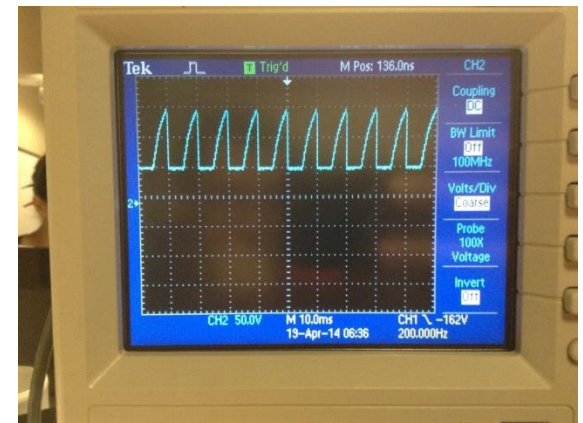
MOSFET 1



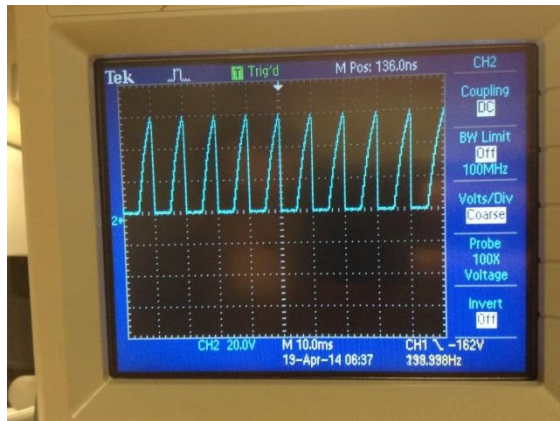
MOSFET 2



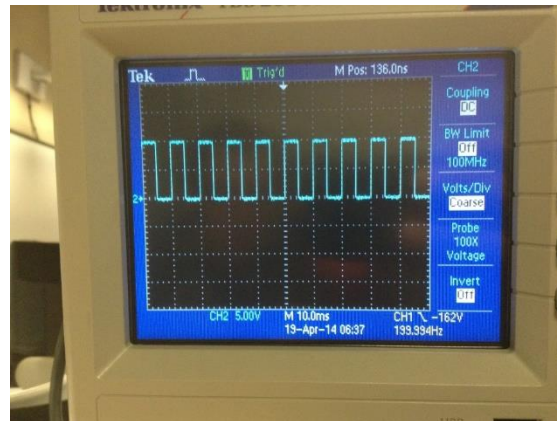
MOSFET 3



MOSFET 4



MOSFET 5



- Peak Voltages:
 - M1: 200 V
 - M2: 140 V
 - M3: 100 V
 - M4: 60 V
 - M5: 10 V

Test 1 – Conclusion

- In short, the Test 1 results were far from expected results
- As discussed, the Zener diodes may be the source of the distorted wave form
- To fix the problem, we will test by using 10K Ohm resistors in place of the Zener diodes