
JAZMINE BOLOOR

boloor@unlv.nevada.edu

(336) 210-6595

EDUCATION | MASTER OF SCIENCE IN ELECTRICAL ENGINEERING – UNIVERSITY OF NEVADA, LAS VEGAS (MAY 2021 – PRESENT)

- Expected Graduation: December 2022

BACHELOR OF SCIENCE IN ELECTRICAL ENGINEERING – UNIVERSITY OF NEVADA, LAS VEGAS (AUGUST 2017 – MAY 2021)

SKILLS | ABILITIES: PCB layout, soldering, wire bonding, FPGA programming

LANGUAGES: MATLAB, C++, Verilog, VHDL

SOFTWARE: Cadence Virtuoso, LTspice, DipTrace, Eagle, PowerWorld, CAD, Quartus

EXPERIENCE | UNLV TEACHING/RESEARCH ASSISTANT, DR. R. JACOB BAKER

MARCH 2019 - PRESENT

- Teaching assistant and grader for two graduate level courses on Mixed-Signal Design and Memory Circuit Design
- Researched, designed, and tested various Printed Circuit Boards (PCBs) on Eagle and Diptrace; wire bonded using K&S Wire Bonder (4526); soldered various PCBs
- Researched and designed various 3D printed models using CAD software

ENGINEERING INTERN, SOUTHWEST GAS

MAY 2019 - AUGUST 2019

- Member of the Distribution Integrity Management Program team, where distribution pipeline leak analysis data is analyzed to optimize leak prevention
- Worked with a team of testers on the launch of a new application that provides a central location to organize and update thousands of leak records; authored a user manual for application

MANAGER, CAFE ZUPAS

NOVEMBER 2015 - JUNE 2018

- Managed 7-9 servers per shift while providing strong customer service skills
- Demonstrated strong leadership skills by training over fifty new hires

- PROJECTS |**
- Created a machine learning algorithm that predicts the likelihood of an inputted patient having heart disease based on their specific symptoms
 - Researched and presented a diagnosis for a patient in Atrial Ventricular Reentrant Tachycardia with a focus on how electrical signals travel throughout the heart
 - Designed a schematic and layout for a Flyback switching power supply controller chip, as well as a high speed, low power digital receiver circuit in Cadence Virtuoso
 - Designed a low voltage operational amplifier with high DC open-loop gain (over 60-80 dB for various VDDs) that produces a quiescent current draw of less than 1 mA and a gain-bandwidth product of over 5.8MHz using LTspice
 - Researched and presented on different photovoltaic modules with a focus on the results of bifacial PV modules