Dane L. Gentry 175 Adomeit Drive Henderson, Nevada 89074

Electrical engineer committed to applying expertise and innovative approaches to analog, digital, and mixed-signal integrated circuit design and testing through collaboration with a professional firm.

Relevant Experience

- Graduate Research Assistant to Dr. R. Jacob Baker (December 2016 Present)
- Undergraduate Research Assistant to Dr. R. Jacob Baker (June 2015 December 2016)
- Early Outreach College Readiness Facilitator (June 2016)

Education

University of Nevada, Las Vegas (UNLV)

- M.S. degree in Electrical Engineering (January 2017 December 2018)
 - o GPA 3.93
- B.S. degree in Electrical Engineering and Mathematics minor (December 2016)
 - o GPA 3.51

Projects - Project details found at cmosedu.com/jbaker/students/dane/dane.htm

- Design, Layout, and Testing of SiGe Avalanche Photodiodes Fabricated in a BiCMOS Process (Thesis)
- Continuous-Time 8-Path KD1S (8D1S) ADC
- Wire Bonding Manual
- Smart Home (User-Tracking) Speaker System (Senior Design Project)
- Transimpedance Amplifier
- Voltage Follower
- Negative Charge Pump
- Passive 2nd-Order Sigma-Delta Modulator for Low-Power ADC
- Solid-State Guitar Amplifier
- PWM Frequency Regulated AC/DC Rotary Convertor

Proficient with:

- LTspice, PSpice, and PowerWorld
- Cadence (Set up following Process Design Kits (PDKs): C5, S35, 7RF, 8RF, 8HP)
- FreePCB, EAGLE PCB, and Altium PCB
- Silvaco (Set up license server)
- MATLAB and Simulink
- Arduino IDE

Professional Memberships

- UNLV Student Technology Advisory Board (STAB) (2018 Present)
- UNLV Graduate & Professional Student Association (GPSA) (2017 Present)
- UNLV Graduate Awards Committee (2017 Present)
- Institute of Electrical and Electronics Engineers (IEEE) (2014 Present)

References

Dr. R. Jacob Baker (PhD, PE)
Professor of Electrical and Computer Engineering
University of Nevada, Las Vegas
rjacobbaker@gmail.com