

**DON'T MISS THIS**

On Feb. 19, 20 and 21, Al Socolovsky (EDN's Eastern editor) and I attended the 1964 International Solid-State Circuits Conference in Philadelphia. Over the years I have attended quite a number of conferences, seminars and technical meetings and for the most part they leave me cold. Poor papers, speakers and presentations at many of these get-togethers do nothing to help the designer in his work. The information obtained may be interesting history or futuristic star-gazing.

ISSCC was an exception. The three days of parallel sessions and the numerous informal evening discussions were excellent. The committees responsible for the session scheduling and paper selection are to be commended. The facilities were ample (with the possible exception that the evening discussion on FET's could have been held in a larger room). The speakers had something to say—and they said it. The panels presiding at the evening discussions were superior to any I have encountered. The attendees participated actively. Product pushing (buy our stuff) was almost nonexistent. What a great change from the other meetings I have attended.

By all means, plan to attend the next ISSCC. Talk it up in your group. Tell your supervisors and employees about it.

To convince yourself, send \$5 to H. G. Sparks, The Moore School of Electrical Engineering, University of Pennsylvania, 200 S. 33rd St., Philadelphia 4, Pa., for a copy of the 1964 proceedings. Read these proceedings. The applications of solid-state circuits to designs from d-c to light are covered.

This conference is really worthwhile.

*Lawrence L. Rosine*

Editor

**V<sub>OUT</sub>**

**-V<sub>SS</sub>**

**0°C**

**NORMALIZED I<sub>D</sub>**

Values of I<sub>D</sub> and g<sub>m</sub> for operating coefficient

I <sub>D</sub> (mA)	I <sub>D</sub> (mA)	I <sub>D</sub> (mA)
0.5149	2N2841	14.0150
0.0120	2N2842	40.0140
5.01370	2N2843	125.450
0.01100	2N2844	400.1300

Scale: 0.00025

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OR IN MATCHED  
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