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AT 1958 TRANSISTOR AND SOLID-STATE CIRCUITS CONFERENCE
IN PHILADELPHIA

SESSION CHAIRMEN and members of the National Committee of the 1958 Transistor and Solid-State Circuit Conference (Feb. 20-21) at general headquarters in the Sheraton Hotel: Front row (left to right)- J. A. Rajchman (RCA), H. E. Tompkins (U of P), John G. Brainerd (U of P), Sidney S. Shamis (NYU), Lewis Winner; second row (left to right)- P. M. Thompson (Defense Research-Ottawa, Canada), Ralph M. Showers (U of P), Robert Mayer (Minneapolis-Honeywell), Howard T. Mooers (Minneapolis-Honeywell), N. A. Koss (GE); third row (left to right)- George Royer (IBM), Richard H. Baker (MIT), James H. Mulligan Jr. (NYU), J. J. Suran (GE).



