

IRE — AIEE

G. W. SHERARD
SCSA

Conference on
transistor circuits

IRE-AIEE CONFERENCE ON TRANSISTOR CIRCUITS

Your registration card with the following items checked was received:

Registration	<input checked="" type="checkbox"/>
Thursday Lunch	<input checked="" type="checkbox"/>
Cocktail Buffet	<input checked="" type="checkbox"/>
Friday Lunch	<input checked="" type="checkbox"/>

Your registration badge and indicated meal tickets will be available at the conference advance Registration Desk (see program flyer).

H. Good
RCA Victor Division
Camden 2, N. J.

No 215

IRE-AIEE Conference on Transistor
Circuits

COCKTAIL-BUFFET - Feb. 18

6:00-7:30 P. M.

PENN-SHERWOOD HOTEL

IRE-AIEE Conference on Transistor
Circuits

LUNCH - February 18

12:30-1:30 P. M.

Rotunda of the University Museum

No 353

IRE-AIEE Conference on Transistor
Circuits

LUNCH - February 19

12:30-1:30 P. M.

Rotunda of the University Museum

No 374