H.W. #19 EE 420/ECG 620 Spring 2019

Show your work for credit and put a box around each of your answers (follow the hw guidelines!)

1. Do book problems: 24.1, 24.3, 24.6, 24.8, and label all DC voltages (in terms of *V*_{DS,sat}, *V*_{SD,sat}, *v*_{SD,sat}, and the threshold voltages assuming the input voltage is *VDD*/2) in Fig. 24.29. (3 points each)