H.W. #22 EE 220 Fall 2017
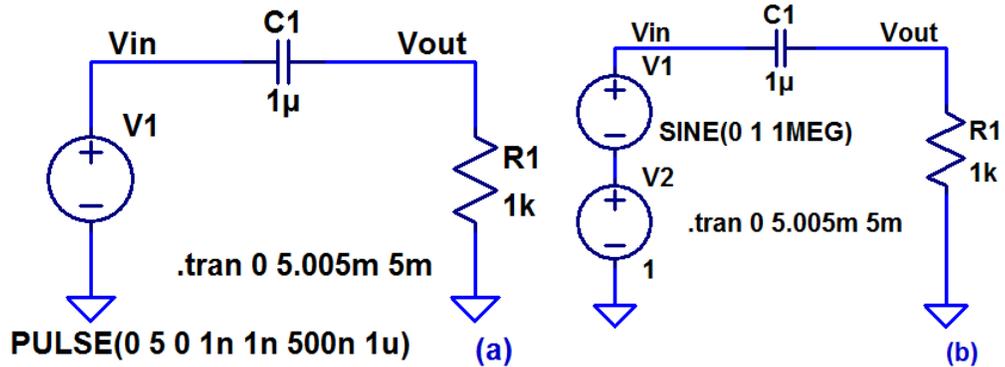
Show your work for credit!

1. In steady-state conditions what is Vout? When the input signal first starts, what is Vout? Why? Verify your answers with four LTspice simulations (one for each case, that is, when the simulation starts saving data at 0 and at 5 ms [5 time constants]). These circuits are practically important and demonstrate AC coupling an input signal. (4 points)

(a)   (b)

2. Determine Vout for each of the following circuits. Sketch Vout and Vin on the same plot. Show your hand calculations for credit. Verify your answers using LTspice. (8 points)

(a)   (b)

(c)   (d)