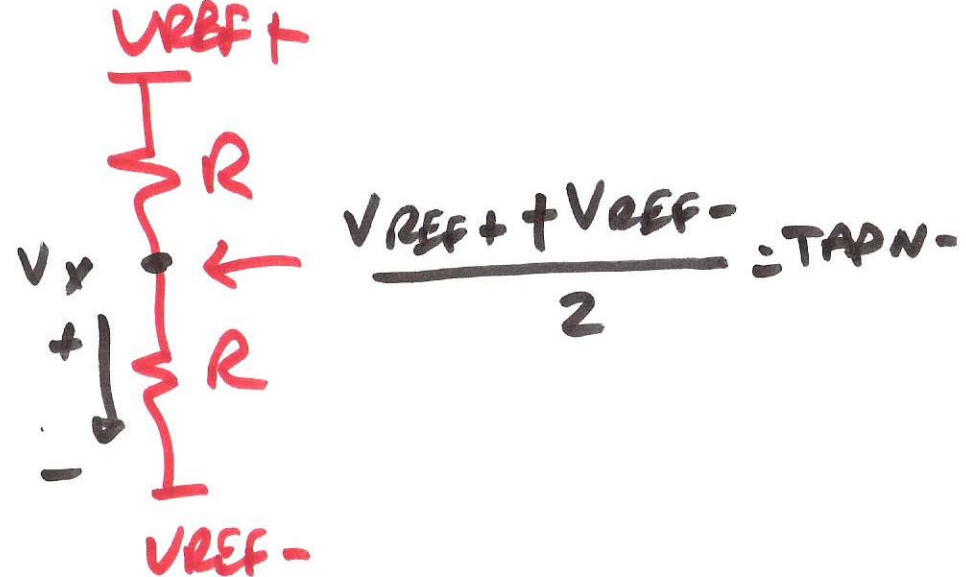
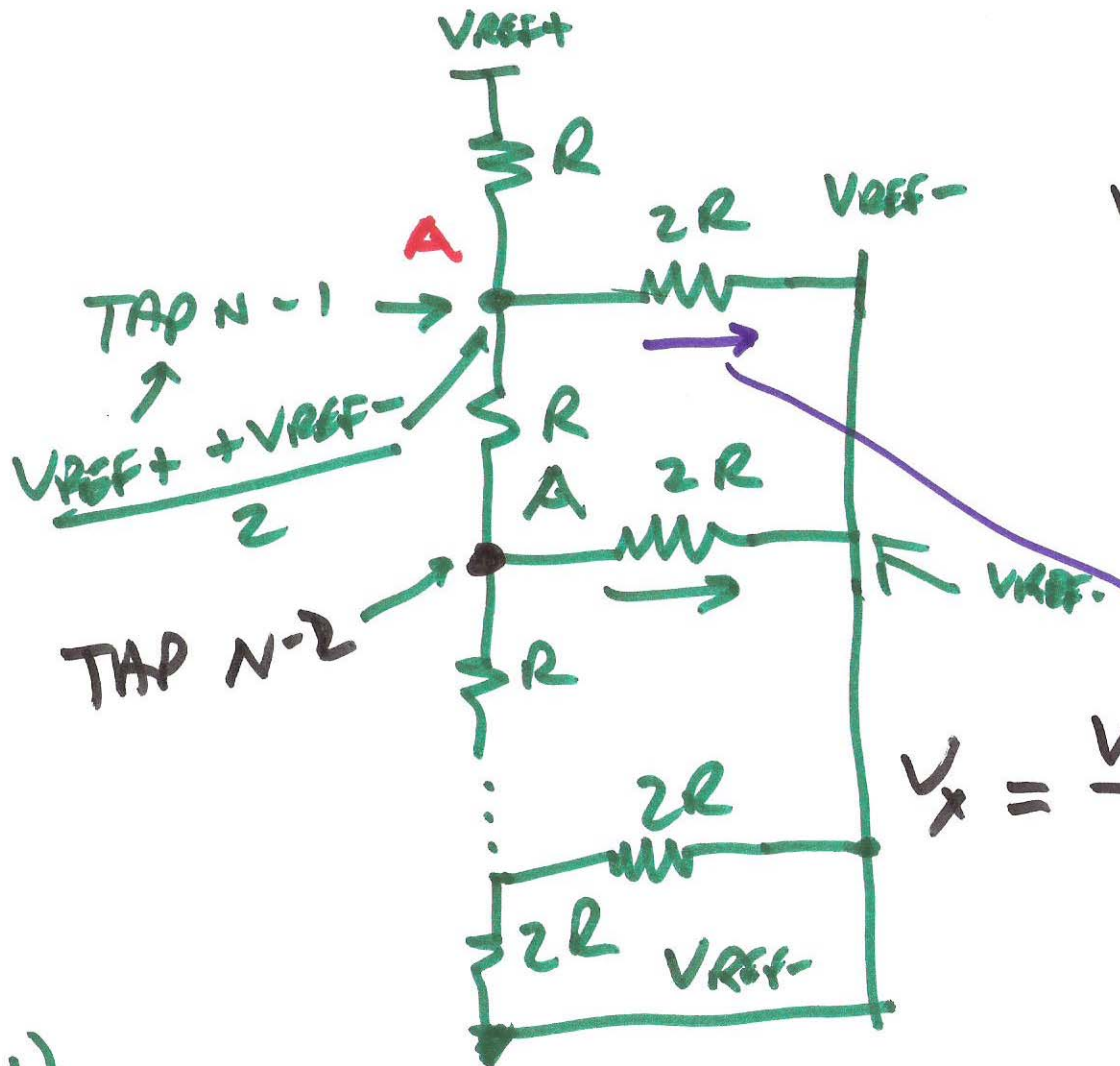


NOV. 1, 2011

Advanced Analog IC Design

11/1/11

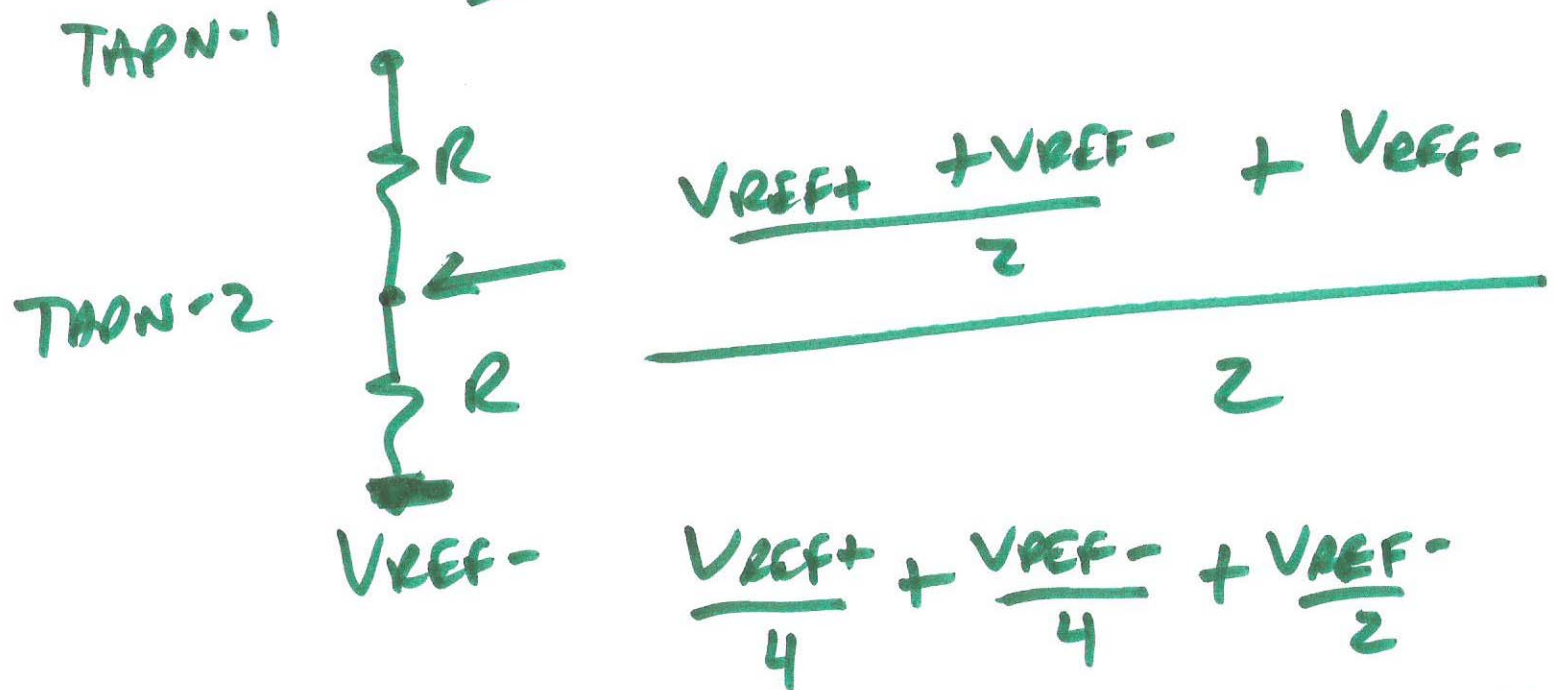


$$V_x = \frac{V_{REF+} - V_{REF-} \cdot R}{2R} = I \cdot R + V_{REF-}$$

1)

$$V_x = \frac{V_{REF+} - V_{REF-}}{2} + \frac{V_{REF-} \cdot 2}{2}$$

$$= \frac{V_{REF+} + V_{REF-}}{2}$$



$$I = \frac{1}{R} \left(\frac{V_{REF+}}{4} + \frac{3}{4} V_{REF-} - V_{REF-} \right)$$

2)

Voltage at tap $X =$

$$\frac{2^X}{2^N} (V_{REF+} - V_{REF-}) + V_{REF-}$$

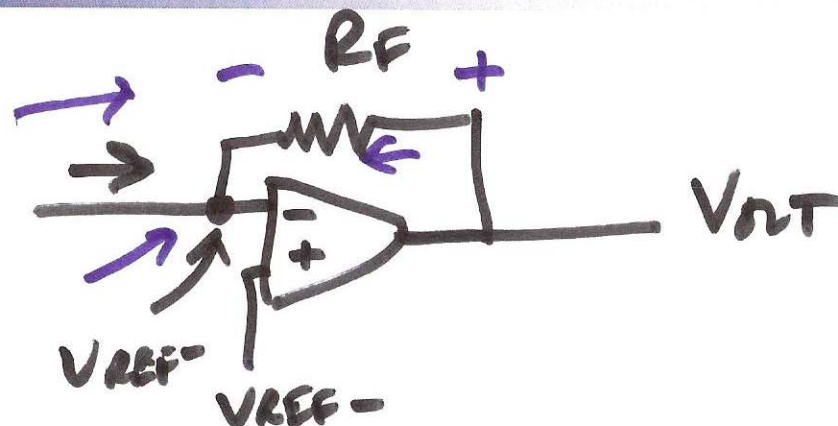
$$0 \leq X \leq N-1 \quad ***$$

$$\frac{2^{N-2}}{2^N} (V_{REF+} - V_{REF-}) + V_{REF-}$$

$$I_{TAP X} = \frac{1}{R} \left(\frac{2^X}{2^N} (V_{REF+} - V_{REF-}) + V_{REF-} - V_{REF-} \right)$$

EQ. 30.2

3)



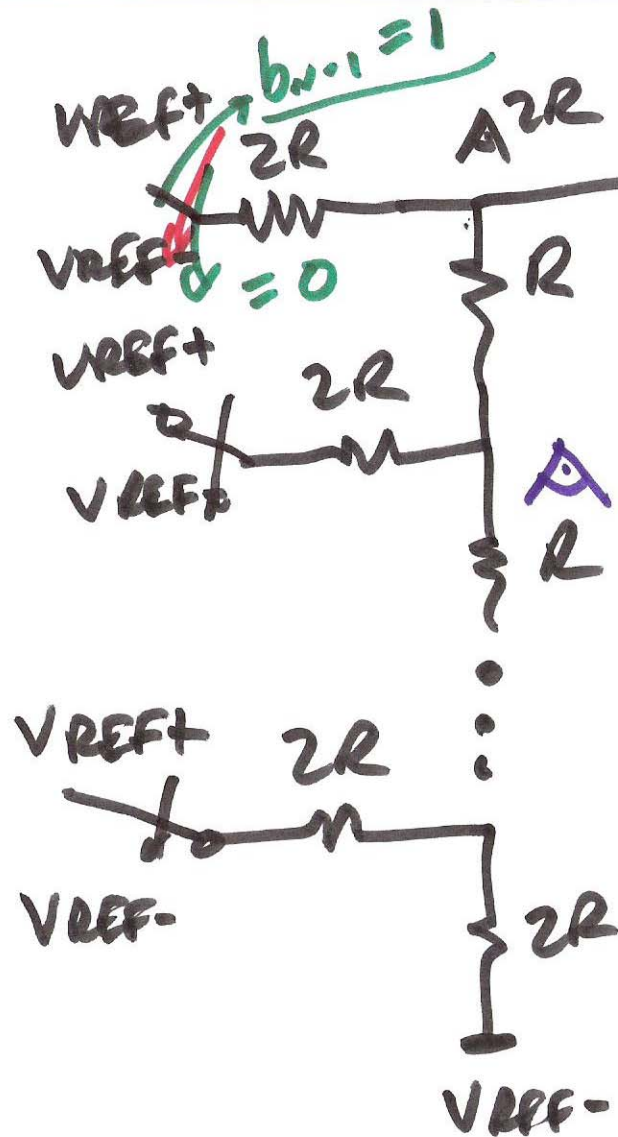
$$V_{OLT} = - \frac{1}{R} \left(\frac{2^x}{2^N} (V_{REF+} - V_{REF-}) \right) R + V_{REF-}$$

(due to tap x)

$$V_{OLT} = V_{REF-} + \sum_{x=0}^{N-1} (b_x \cdot I_{TAPx}) \cdot \frac{R'}{R}$$

$V_{REF+} > V_{REF-}$

4)



$$V_{out} = \frac{V_{REF+} + V_{REF-}}{2} +$$

$$= b_{n-1} \cdot V_{REF+} + b_{n-1} \cdot V_{REF-}$$

$$2R \frac{V_{REF-} + V_{REF-}}{2}$$

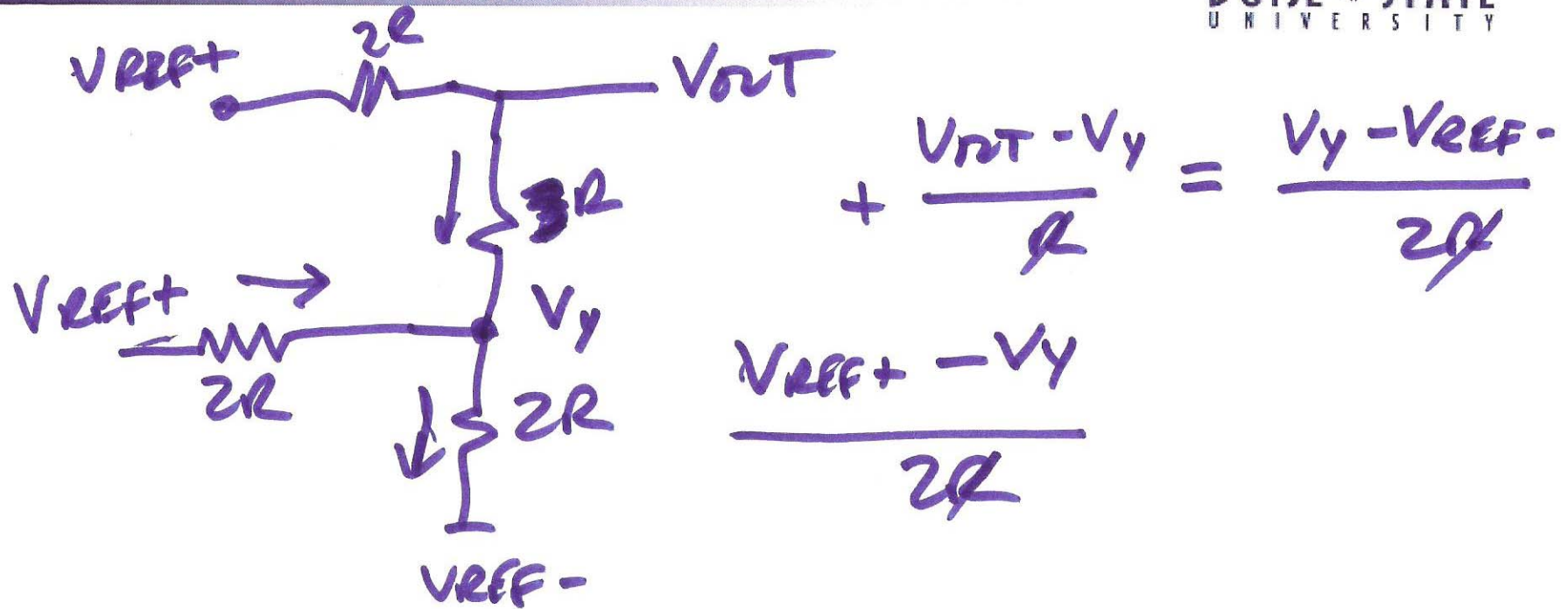
$$\left. \begin{matrix} 2R \\ \vdots \\ 2R \end{matrix} \right\} 3R$$

$$\left. \begin{matrix} 2R \\ \vdots \\ 2R \end{matrix} \right\} 2R$$

$$\downarrow$$

$$V_{REF-}$$

s)



$$V_{OUT} = \left(\frac{V_{OUT} - V_Y}{R} \right) R + V_Y$$

$$2V_{OUT} - 2V_Y = V_Y - V_{REF-} + V_{REF+} - V_Y$$

$$V_Y = \frac{2V_{OUT} - V_{REF+} + V_{REF-}}{2}$$

b)

$$V_{OUT} = V_Y \cdot \frac{R}{2R+R} + V_{REF+}$$

$$= \frac{V_Y}{3} + V_{REF+}$$

$$V_{OUT} = \left(V_{OUT} - \frac{V_{REF+}}{2} + \frac{V_{REF-}}{2} \right) \frac{1}{3} + V_{REF+}$$

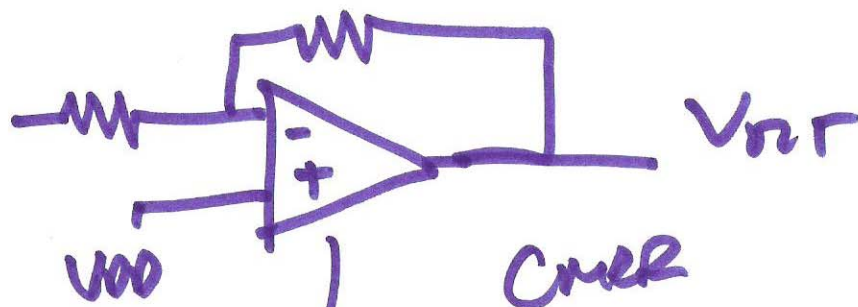
$$3V_{OUT} = V_{OUT} - \frac{V_{REF+}}{2} + \frac{V_{REF-}}{2} + 3V_{REF+}$$

$$V_{OUT} = -\frac{V_{REF+}}{4} + \frac{V_{REF-}}{8} + \frac{3}{2}V_{REF+}$$

$$= \frac{5}{4}V_{REF+} + \frac{1}{4}V_{REF-}$$

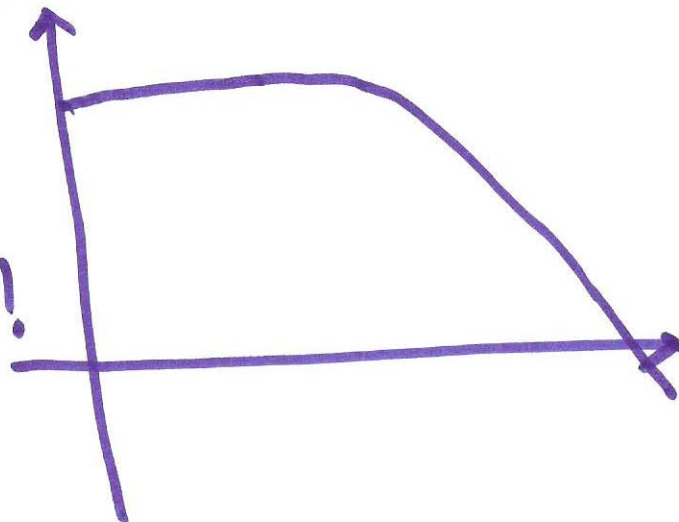
$$= \frac{1}{4}(V_{REF+} + V_{REF-}) + V_{REF+}$$

7)



C_{max}

Not good for low distortion!



8)

$$\Delta I = \frac{V_{REF+} - V_{REF-}}{2(R - \Delta R)} \left[1 - \frac{1}{2^{N-1}} \right]$$

$$- \frac{V_{REF+} - V_{REF-}}{2(R + \Delta R)} \approx$$

INL Calculation.

$$\frac{\Delta R}{R} \leq \frac{1}{2^{N-1}}$$

$$\left| \frac{\Delta R}{R} \right| \leq \frac{1}{2^{N-1}}$$

10 bit converter

$$1 \text{ LSB INL} \quad \frac{1}{2^9} = \frac{1}{512} = \underline{\underline{0.2\%}}$$

mismatch

9)