FNT - Fowler Northern Tunnel
CHE - Channel Hot Electron
10 → 20
7

Poly2
-30μm
230μm

Deep N-Well
P-Well
CHE (Lucky electron model)

\[ I_D = \frac{\beta}{2} (V_{GS} - V_{TH})^2 \]

NAND
FNT → program erase

10 years
2 × 10^30

Self-limiting
NOR Flash

All Vth values are positive.

NAND - Flash used for mass storage.
NOR - Flash used for faster NOR.
SLC - Single-Level Cell

Diagram:

- SLC schematic with various components labeled:
  - MUX
  - PW
  - CLX Sense Amp
  - WL goes low
  - MEG +5V

Description:

- The diagram illustrates a single-level cell with various electronic components and labels indicating their functions.
- MUX and PW are highlighted with specific labels indicating their roles in the circuit.
- The WL signal is mentioned to go low, indicating a state change within the circuit.
MLC $\rightarrow$ 2 bits/Cell

WL $\rightarrow$ 0

Diagram showing electrical circuits and states.
Bitline mux

met10

32\text{nm} = L = W

5\text{nm}

100\text{nm}

SA

pitch cells

L = 0.25\text{nm}