Basic
- CMOS imager
- Questions
- Pixel implementation
- DSM sensing
- Resistor memory
- Phase change
- PCRAM
- Programmable contact
- Sensing resistive memory
- DSM sensing
- Flash sensing using DSM.
Basic Flash memory operation

writing
erasing
NOR programming
CHE
FET
Layout of Flash
X-sect views (why triple well?)

Why program from bottom up?

NOR, OTP

SRAM → 6T Cell operation

3)
Word line delay
Bit line capacitance

Decoders
Word line drivers (Fig. 16.46)
I/O transistors

Array/pentil schematics

N-sense and P-sense amp
EQ - circuit
I/O - devices
WL - driver

Decoders
Global vs. Local
Timing info for reading & writing DRAM

Design of sense amplifiers

Kickback noise balance impedance inputs
Memory (no)
Low power (no)
Content DC current
OSM
I/O helper FF pipelining