H.W. #6 CpE 100 Spring 2021

Show your work for credit (!) and put a box around each of your answers!

1. Make schematics and corresponding symbol views for 3-input NAND, NOR, AND, and OR gates. Simulate the operation of your gates to show they function properly. (8 points)
2. Write the truth table for $A\bar{B} + C$ and sketch the logic gate implementation. Then simulate your logic gate implementation to verify your truth table is correct. (4 points)