H.W. #11 CpE 100 Spring 2021

Show your work for credit (!) and put a box around each of your answers if possible!

1. Work Exercise 2.28 on page 101 of the textbook using Karnaugh maps and sketch the gate implementation. Using simulations show that your implementation matches the truth table. (6 points)
2. Simulate the operation of an SR latch using NOR and NAND gates. Comment on the simulation output to show you know what you are doing. (4 points)